NCE1520KA

NCE N-Channel Enhancement Mode Power MOSFET

Description

The NCE1520KA uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

V_{DS} = 150V,I_D =20A

 $R_{DS(ON)} < 75 m\Omega @ V_{GS} = 10V \quad (Typ:62 m\Omega)$

 $R_{DS(ON)}$ <80m Ω @ V_{GS} =4.5V (Typ:68m Ω)

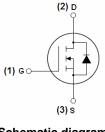
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation

Application

- Boost converters
- LED backlighting
- Uninterruptible power supply

100% UIS TESTED!

100% ΔVds TESTED!



Schematic diagram



Marking and pin assignment



TO-252 -2Ltop view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE1520KA	NCE1520KA	TO-252-2L	-	-	-

Absolute Maximum Ratings (T_C=25 ℃unless otherwise noted)

Symbol	Parameter	Limit	Unit
V _{DS}	Drain-Source Voltage	150	V
V _G S	Gate-Source Voltage	±20	V
I _D	Drain Current-Continuous	20	А
I _D (100℃)	Drain Current-Continuous(TC=100℃)	14	Α
I _{DM}	Pulsed Drain Current	40	Α
P _D	Maximum Power Dissipation	90	W
	Derating factor	0.6	W/℃
E _{AS}	Single pulse avalanche energy (Note 5)	80	mJ
T_{J}, T_{STG}	Operating Junction and Storage Temperature Range	-55 To 175	$^{\circ}$ C



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Thermal Characteristic

R _{eJC}	Thermal Resistance, Junction-to-Case (Note 2)	1.7	°C/W
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Electrical Characteristics (T_C=25 °C unless otherwise noted)

	Symbol Parameter	Condition	Min	Тур	Max	Unit
Off Characterist	tics					•
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V I _D =250μA	150	165	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =150V,V _{GS} =0V	-	-	1	μΑ
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characterist	ics (Note 3)					
$V_{GS(th)}$	Gate Threshold Voltage	V _{DS} =V _{GS} ,I _D =250μA	1.2	1.6	2.5	V
В	Drain-Source On-State Resistance	V _{GS} =10V, I _D =10A	-	62	75	mΩ
$R_{DS(ON)}$	Diam-Source On-State Resistance	V _{GS} =4.5V, I _D =10A -		68	80	mΩ
g FS	Forward Transconductance	V _{DS} =5V,I _D =10A	-	20	-	S
Dynamic Charac	cteristics (Note4)					
C _{lss}	Input Capacitance	\/ -75\/\/ -0\/	-	2500	-	PF
C _{oss}	Output Capacitance	V_{DS} =75V, V_{GS} =0V, F=1.0MHz	-	68	-	PF
C _{rss}	Reverse Transfer Capacitance	F=1.UIVITZ	-	54	-	PF
Switching Chara	acteristics (Note 4)					
t _{d(on)}	Turn-on Delay Time		-	18.5	-	nS
t _r	Turn-on Rise Time	V_{DD} =75 V , R_L =5 Ω	-	10	-	nS
t _{d(off)}	Turn-Off Delay Time	V_{GS} =10 V , R_{GEN} =3 Ω	-	22	-	nS
t _f	Turn-Off Fall Time		-	8	-	nS
Qg	Total Gate Charge	\/ -75\/1 -404	-	60	-	nC
Q _{gs}	Gate-Source Charge	$V_{DS} = 75V, I_{D} = 10A,$ $V_{GS} = 10V$	-	7.1	-	nC
Q _{gd}	Gate-Drain Charge	VGS=10V	-	17	-	nC
Drain-Source Di	iode Characteristics					
V _{SD}	Diode Forward Voltage (Note 3)	V _{GS} =0V,I _S =20A	-	-	1.2	V
Is	Diode Forward Current (Note 2)	-	-	-	20	Α
t _{rr}	Reverse Recovery Time	TJ = 25°C, IF = 10A	-	34	-	nS
Qrr	Reverse Recovery Charge	$di/dt = 100A/\mu s^{(Note3)}$	-	55	-	nC
t _{on}	Forward Turn-On Time	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				y LS+LD)

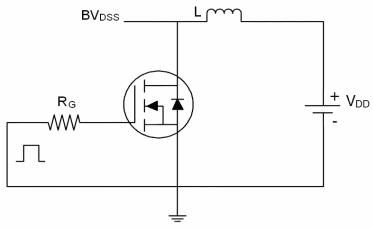
Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, $t \le 10$ sec.
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production
- 5. EAS condition:Tj=25 $^{\circ}\text{C}$,V $_{DD}$ =50V,V $_{G}$ =10V,L=0.5mH,Rg=25 Ω

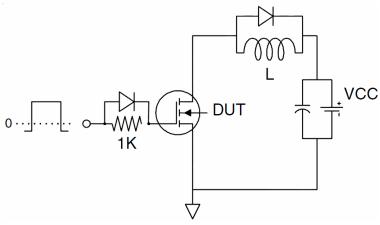
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Test Circuit

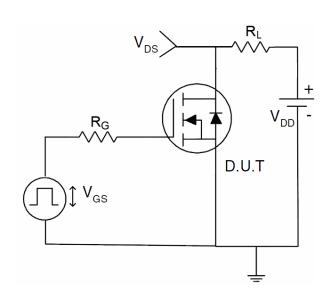
1) E_{AS} Test Circuit



2) Gate Charge Test Circuit



3) Switch Time Test Circuit





Typical Electrical and Thermal Characteristics (Curves)

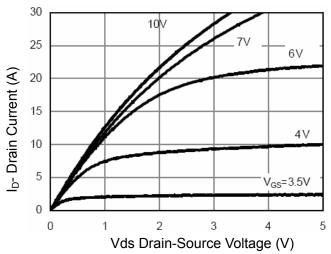


Figure 1 Output Characteristics

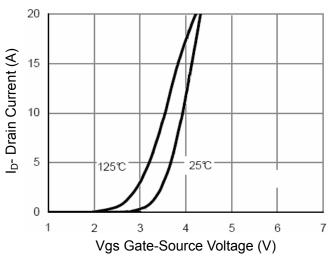


Figure 2 Transfer Characteristics

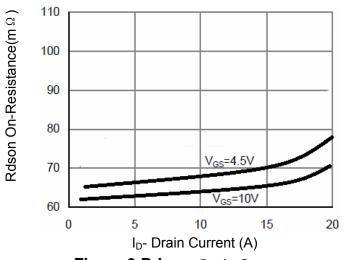


Figure 3 Rdson- Drain Current

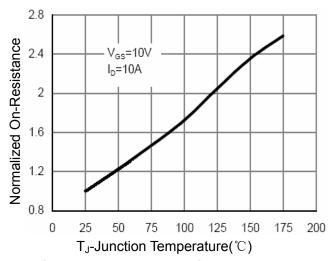


Figure 4 Rdson-JunctionTemperature

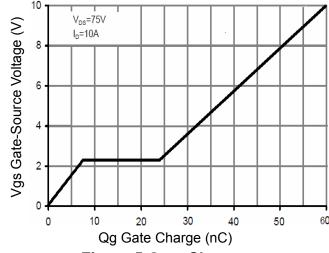


Figure 5 Gate Charge

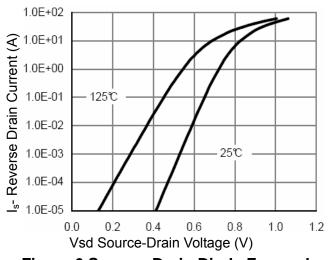


Figure 6 Source- Drain Diode Forward



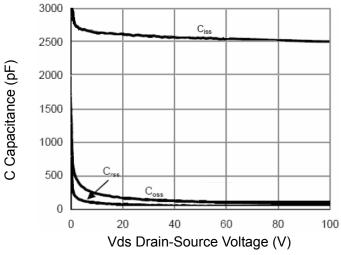


Figure 7 Capacitance vs Vds

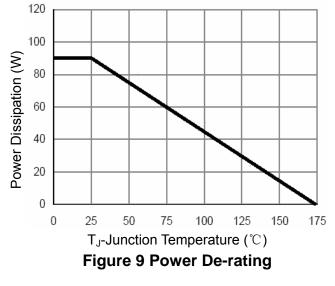


Figure 8 Safe Operation Area

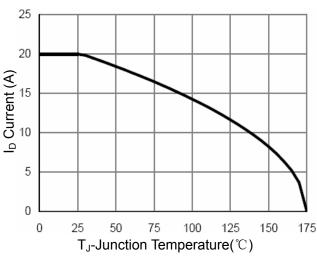
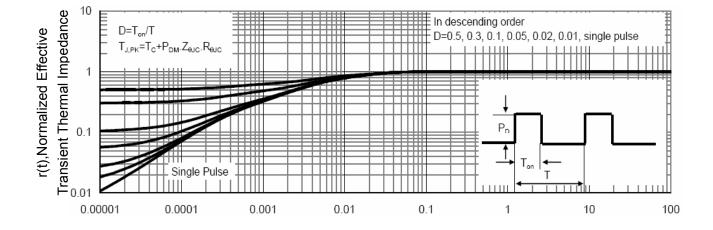


Figure 10ID Current- Junction Temperature



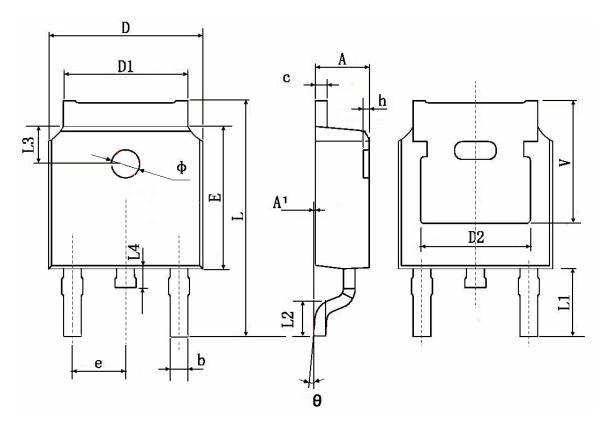
Square Wave Pluse Duration(sec)

Figure 11 Normalized Maximum Transient Thermal Impedance

Pb Free Product



TO-252 Package Information



Comphal	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
А	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.660	0.860	0.026	0.034	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	4.83	30 TYP.	0.190	TYP.	
E	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.800	10.400	0.386	0.409	
L1	2.90	0 TYP.	0.114	TYP.	
L2	1.400	1.700	0.055	0.067	
L3	1.600 TYP.		0.063 TYP.		
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.35	0 TYP.	0.211 TYP.		



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