NCE N-Channel Enhancement Mode Power MOSFET

Description

The NCE5055K uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

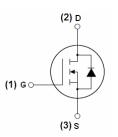
- V_{DS} =50V,I_D =55A
 - $R_{DS(ON)}$ <12m Ω @ V_{GS} =10V
 - $R_{DS(ON)}$ <18m Ω @ V_{GS} =4.5V
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

Application

- Load switching
- Hard switched and high frequency circuits
- Uninterruptible power supply



100% ΔVds TESTED! Package Marking and Ordering Information



Schematic diagram



Marking and pin assignment



TO-252-2L top view

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity	
NCE5055K	NCE5055K	TO-252-2L	-	-	-	

Absolute Maximum Ratings (T_C=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	50	V
Gate-Source Voltage	V _G s	±20	V
Drain Current-Continuous	I _D	55	Α
Drain Current-Continuous(T _C =100℃)	I _D (100℃)	38.9	Α
Pulsed Drain Current	I _{DM}	200	Α
Maximum Power Dissipation	P _D	65	W
Derating factor		0.43	W/℃
Single pulse avalanche energy (Note 5)	Eas	230	mJ
Operating Junction and Storage Temperature Range	T_{J} , T_{STG}	-55 To 175	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Case ^(Note 2)	$R_{ heta JC}$	2.3	°C/W



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Electrical Characteristics (T_C=25 ℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	50	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =45V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)			250uA 12 10 25			
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	1.2	1.9	2.5	V
Drain-Source On-State Resistance	В	V _{GS} =10V, I _D =20A	-	9.6	12.5	mΩ
Dialii-Source Oil-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =15A		12.5	17	11177
Forward Transconductance	g FS	V _{DS} =5V,I _D =20A	20	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{Iss}	\/ -05\/\/ -0\/	-	1760	-	PF
Output Capacitance	Coss	V_{DS} =25V, V_{GS} =0V, F=1.0MHz	_	169	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.UIVITZ	-	123	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}		-	6.1	-	nS
Turn-on Rise Time	t _r	V_{DD} =25 $V_{,,}R_{L}$ =1 Ω	-	17	-	nS
Turn-Off Delay Time	$t_{d(off)}$	V_{GS} =10 V , R_{G} =3 Ω	-	29	-	nS
Turn-Off Fall Time	t _f		-	16.5	-	nS
Total Gate Charge	Q_g	V -25V/1 -20A	-	35.4		nC
Gate-Source Charge	Q _{gs}	V _{DS} =25V,I _D =20A,	-	4.3		nC
Gate-Drain Charge	Q_{gd}	V _{GS} =10V	-	10.5		nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =20A	-		1.2	V
Diode Forward Current (Note 2)	Is		-	-	55	Α
Reverse Recovery Time	t _{rr}	$T_J = 25^{\circ}C, I_F = 20A$	-	29	-	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs ^(Note3)	-	26	-	nC

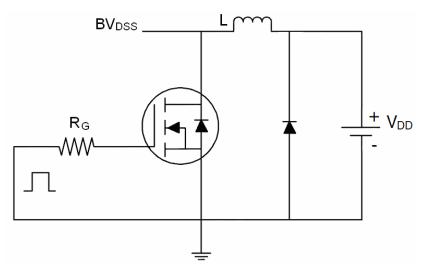
Notes:

- $\textbf{1.} \ \textbf{Repetitive Rating: Pulse width limited by maximum junction temperature}.$
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.
- **4.** Guaranteed by design, not subject to production
- **5.** E_{AS} condition : Tj=25 $^{\circ}$ C, V_{DD} =20V, V_{G} =10V,L=0.5mH,Rg=25 Ω ,

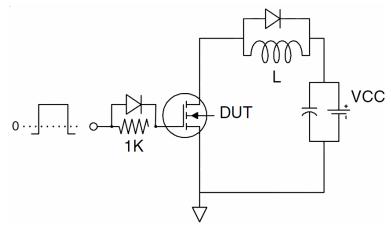


Test circuit

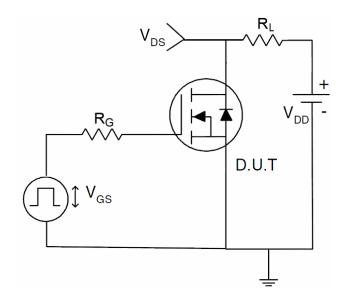
1) E_{AS} Test Circuit



2) Gate Charge Test Circuit



3) Switch Time Test Circuit



Typical Electrical and Thermal Characteristics (Curves)

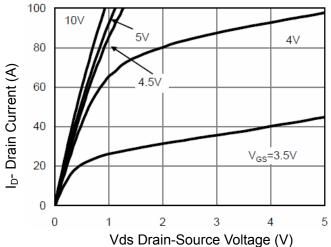


Figure 1 Output Characteristics

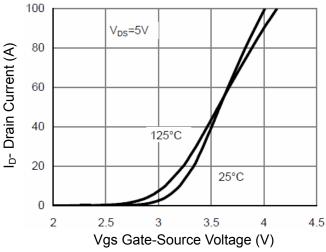
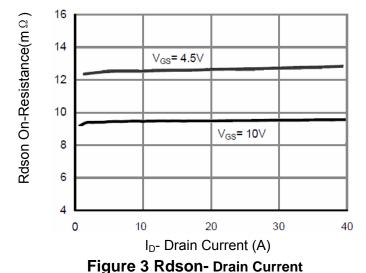


Figure 2 Transfer Characteristics



1.8 V_{GS}=10V I_D=20A 1.4 V_{GS}=10V 1.2 1.4 V_{GS}=10V 1.4 V_{GS}=10V 1.2 1.4 V_{GS}=10V 1

Figure 4 Rdson-JunctionTemperature

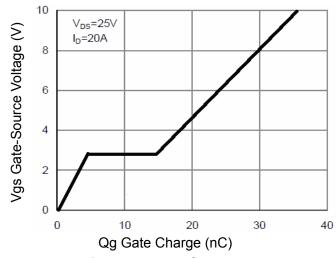


Figure 5 Gate Charge

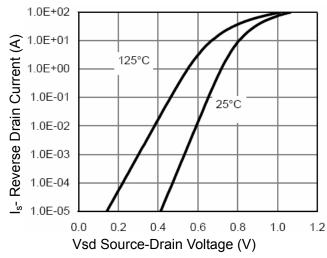
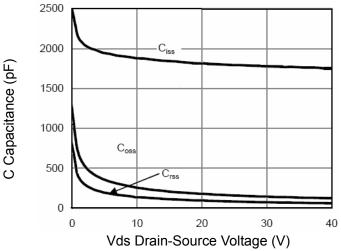


Figure 6 Source- Drain Diode Forward





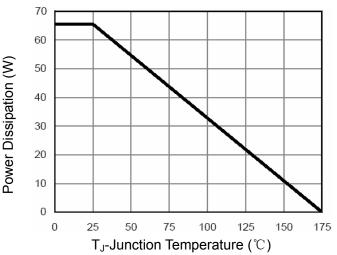
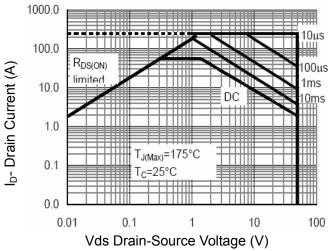


Figure 7 Capacitance vs Vds

Figure 9 Power De-rating



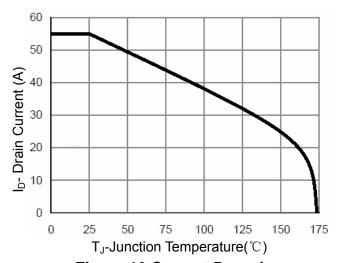
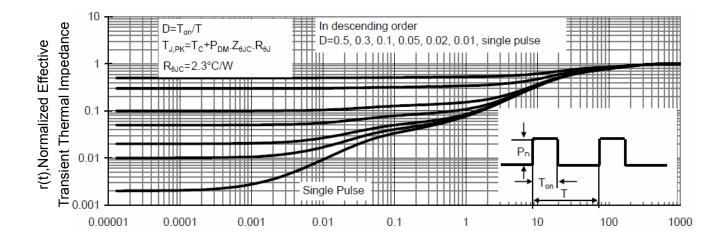


Figure 8 Safe Operation Area

Figure 10 Current De-rating

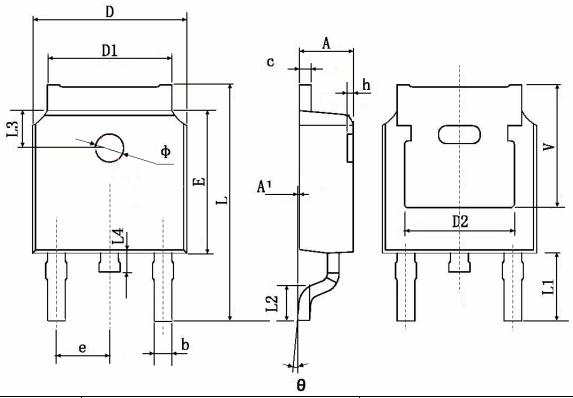


Square Wave Pluse Duration(sec)

Figure 11 Normalized Maximum Transient Thermal Impedance



TO-252 Package Information



Cymbal	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.660	0.860	0.026	0.034	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	4.830	4.830 TYP.		TYP.	
E	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.800	10.400	0.386	0.409	
L1	2.900	TYP.	0.114	TYP.	
L2	1.400	1.700	0.055	0.067	
L3	1.600	1.600 TYP.		TYP.	
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.350	TYP.	0.211 TYP.		



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