

# **NCE N-Channel Super Trench II Power MOSFET**

### **Description**

The series of devices uses **Super Trench II** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of  $R_{DS(ON)}$  and  $Q_g$ . This device is ideal for high-frequency switching and synchronous rectification.

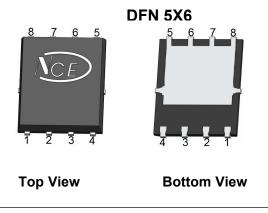
## **Application**

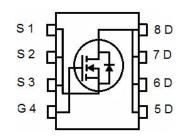
- DC/DC Converter
- •Ideal for high-frequency switching and synchronous rectification

#### **General Features**

- $V_{DS}$  =72V, $I_D$  =120A  $R_{DS(ON)}$ =2.4m $\Omega$  , typical @  $V_{GS}$ =10V
- Excellent gate charge x R<sub>DS(on)</sub> product(FOM)
- Very low on-resistance R<sub>DS(on)</sub>
- 150 °C operating temperature
- Pb-free lead plating

100% UIS TESTED! 100% ΔVds TESTED!





**Schematic Diagram** 

### **Package Marking and Ordering Information**

	Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
Ī	P035N72GU	NCEP035N72GU	DFN5X6-8L	-	-	-

#### Absolute Maximum Ratings (T<sub>c</sub>=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	72	V
Gate-Source Voltage	V <sub>G</sub> s	±20	V
Drain Current-Continuous	I <sub>D</sub>	120	А
Drain Current-Continuous(T <sub>C</sub> =100℃)	I <sub>D</sub> (100℃)	92	А
Pulsed Drain Current	I <sub>DM</sub>	480	А
Maximum Power Dissipation	P <sub>D</sub>	90	W
Derating factor		0.72	W/°C
Single pulse avalanche energy (Note 5)	E <sub>AS</sub>	870	mJ
Operating Junction and Storage Temperature Range	$T_{J}, T_{STG}$	-55 To 150	$^{\circ}$ C

#### **Thermal Characteristic**

Thermal Resistance,Junction-to-Case <sup>(Note 2)</sup>	R <sub>eJC</sub>	1.39	°C/W
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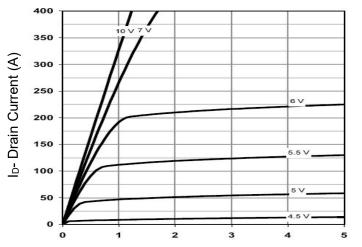
# Electrical Characteristics (T<sub>C</sub>=25°Cunless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics			•	•		
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	72		-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =72V,V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	$V_{GS}$ =±20 $V$ , $V_{DS}$ =0 $V$	-	-	±100	nA
On Characteristics (Note 3)			•	•		
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS},I_{D}=250\mu A$	2.0	3.0	4.0	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =60A	-	2.4	3.5	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =5V,I <sub>D</sub> =60A	75	-	-	S
Dynamic Characteristics (Note4)			'	•		
Input Capacitance	C <sub>lss</sub>	V <sub>DS</sub> =35V,V <sub>GS</sub> =0V, F=1.0MHz	-	4250	-	PF
Output Capacitance	Coss		-	670	-	PF
Reverse Transfer Capacitance	Crss	F=1.UIVIHZ	-	28	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>		-	16	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =35 $V$ , $I_D$ =60 $A$	-	10	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10 $V$ , $R_{G}$ =1.6 $\Omega$	-	35	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	8	-	nS
Total Gate Charge	Qg	\/ 05\/  00A	-	67.5	-	nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}=35V,I_{D}=60A,$	-	18.8		nC
Gate-Drain Charge	$Q_{gd}$	V <sub>GS</sub> =10V	-	13.5		nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =60A	-		1.2	V
Diode Forward Current (Note 2)	Is		-	-	120	Α
Reverse Recovery Time	t <sub>rr</sub>	T <sub>J</sub> = 25°C, I <sub>F</sub> = 60A	-	68	-	nS
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note3)}$	-	98	-	nC

## Notes:

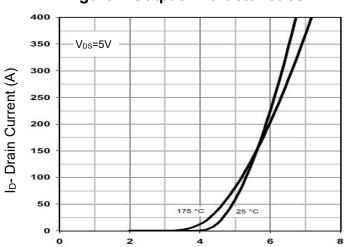
- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width  $\leq$  300 $\mu$ s, Duty Cycle  $\leq$  2%.
- 4. Guaranteed by design, not subject to production
- 5. EAS condition : Tj=25  $^{\circ}\!\!\mathrm{C}$  ,V\_DD=35V,V\_G=10V,L=0.5mH,Rg=25 $\Omega$

## **Typical Electrical and Thermal Characteristics**



Vds Drain-Source Voltage (V)

**Figure 1 Output Characteristics** 



Vgs Gate-Source Voltage (V)

**Figure 2 Transfer Characteristics** 

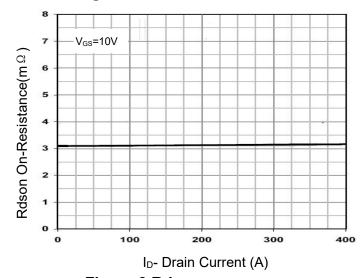
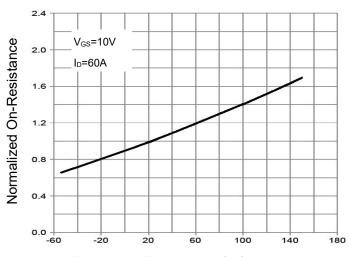


Figure 3 Rdson- Drain Current



T<sub>J</sub>-Junction Temperature(°C)

Figure 4 Rdson-Junction Temperature

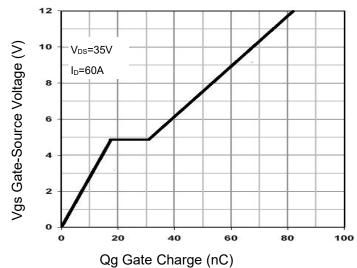
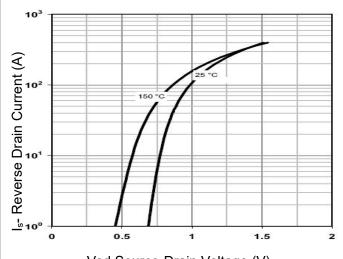
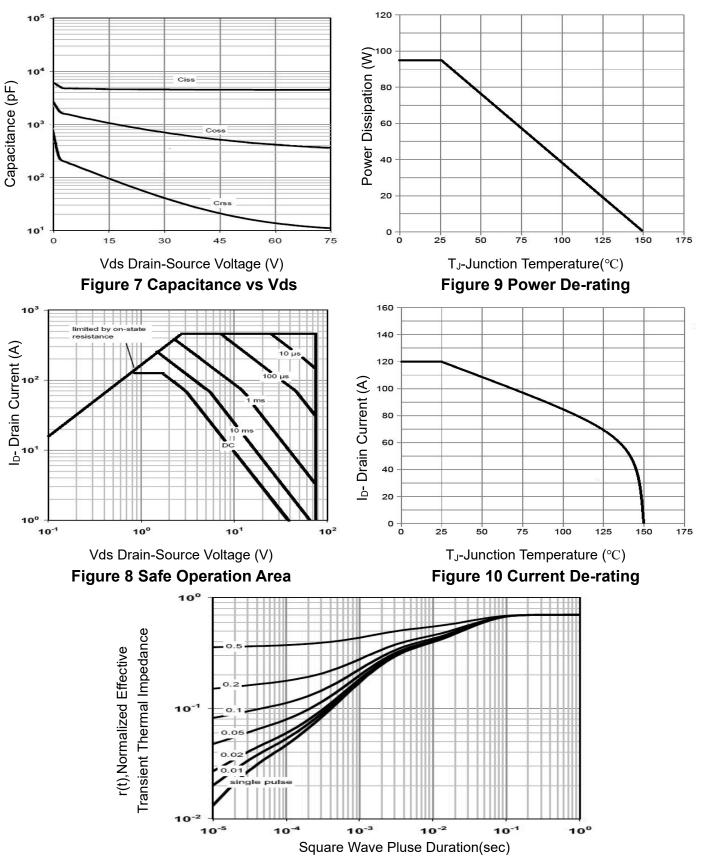


Figure 5 Gate Charge



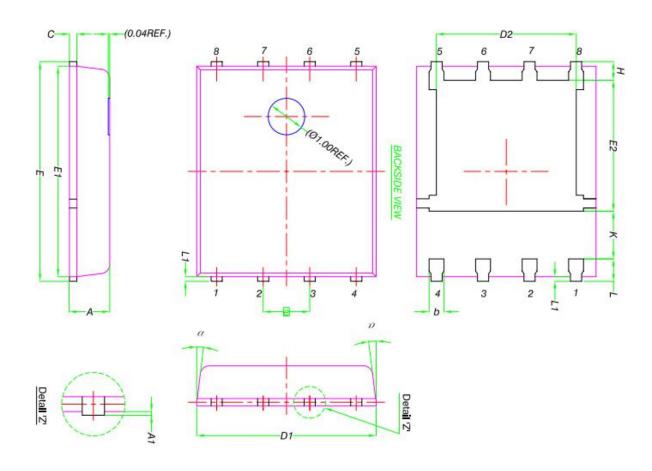
Vsd Source-Drain Voltage (V)

Figure 6 Source- Drain Diode Forward

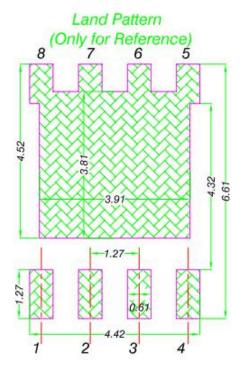


**Figure 11 Normalized Maximum Transient Thermal Impedance** 

# **DFN5X6-8L Package Information**



	N	ILLIMET	ERS	
DIM.	MIN.	NOM.	MAX.	
Α	0.90	1.00	1.10	
A1	0	-	0.05	
b	0.33	0.41	0.51	
С	0.20	0.25	0.30	
D1	4.80	4.90	5.00	
D2	3.61	3.81	3.96	
Ε	5.90	6.00	6.10	
E1	5.70	5.75	5.80	
E2	3.38	3.58	3.78	
е	- 6	1.27 BSC		
Н	0.41	0.51	0.6	
K	1.10	-	-	
L	0.51	0.61	0.71	
L1	0.06	0.13	0.20	
α	0°		12°	



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