

## **NCE N-Channel Super Trench Power MOSFET**

#### **Description**

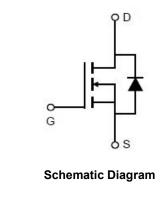
The NCEP0140AG uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of  $R_{\text{DS(ON)}}$  and  $Q_g$ . This device is ideal for high-frequency switching and synchronous rectification.

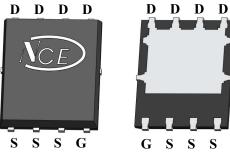
#### **General Features**

- $V_{DS}$  =100V, $I_D$  =40A  $R_{DS(ON)}$ =18m $\Omega$  (typical) @  $V_{GS}$ =10V  $R_{DS(ON)}$ =22m $\Omega$  (typical) @  $V_{GS}$ =4.5V
- Excellent gate charge x R<sub>DS(on)</sub> product(FOM)
- Very low on-resistance R<sub>DS(on)</sub>
- 150 °C operating temperature
- Pb-free lead plating
- 100% UIS tested

## **Application**

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification





100% UIS TESTED!

**Top View** 

100% ΔVds TESTED!

**Bottom View** 

#### **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
P0140AG	NCEP0140AG	DFN5X6-8L	-	-	-

### Absolute Maximum Ratings (T<sub>c</sub>=25°Cunless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	100	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous	I <sub>D</sub>	40	Α
Drain Current-Continuous(T <sub>C</sub> =100 °C)	I <sub>D</sub> (100℃)	28.3	А
Pulsed Drain Current	I <sub>DM</sub>	160	А
Maximum Power Dissipation	P <sub>D</sub>	50	W
Derating factor		0.4	W/℃
Single pulse avalanche energy (Note 1)	E <sub>AS</sub>	200	mJ
Operating Junction and Storage Temperature Range	$T_{J}, T_{STG}$	-55 To 150	$^{\circ}$



# NCEP0140AG

## **Thermal Characteristic**

Thermal Resistance,Junction-to-Case	$R_{ heta JC}$	2.5	°C/W	
Thermal Resistance, Juniction-to-Case	I <b>V</b> BJC	2.5	C/VV	

## Electrical Characteristics (T<sub>C</sub>=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics			•	,		•
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250µA	100		-	٧
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =100V,V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics	·					
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS},I_{D}=250\mu A$	1.2	1.8	2.5	V
Desir Course On Otata Desirtana		V <sub>GS</sub> =10V, I <sub>D</sub> =20A	-	18	23	mΩ
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =20A	-	22	27	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =5V,I <sub>D</sub> =20A	-	35	-	S
Dynamic Characteristics						
Input Capacitance	C <sub>lss</sub>	\/ 50\/\/ 0\/	-	1600	-	PF
Output Capacitance	Coss	$V_{DS}=50V, V_{GS}=0V,$	-	139	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.0MHz	-	11	-	PF
Switching Characteristics (Note 2)						
Turn-on Delay Time	t <sub>d(on)</sub>		-	6	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =50 $V$ , $I_D$ =20 $A$	-	2	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}\text{=}10V, R_{G}\text{=}1.6\Omega$	-	18	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	2	-	nS
Total Gate Charge	Qg	V 50VI 00A	-	26	-	nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =50V,I <sub>D</sub> =20A,	-	7.4		nC
Gate-Drain Charge	Q <sub>gd</sub>	V <sub>GS</sub> =10V	-	3.8		nC
Drain-Source Diode Characteristics						
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =40A	-		1.2	V
Diode Forward Current	Is		-	-	40	Α
Reverse Recovery Time	t <sub>rr</sub>	T <sub>J</sub> = 25°C, I <sub>F</sub> = 20A	-		26	nS
Reverse Recovery Charge	Qrr	di/dt = 500A/μs	-		98	nC

#### Notes:

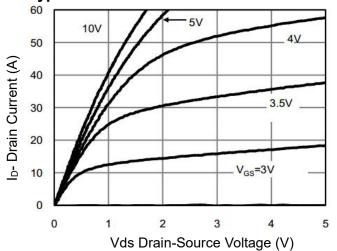
<sup>1.</sup> EAS condition : Tj=25  $^{\circ}\text{C}$  ,VDD=20V,VG=10V,L=0.5mH,Rg=25 $\Omega$ 

<sup>2.</sup> Guaranteed by design, not subject to production .

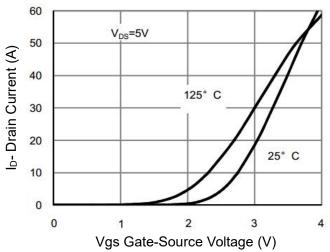
<sup>3.</sup> These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsin k, assuming a maximum junction temperature of TJ(MAX)=150° C. The SOA curve provides a single pulse rating.



## **Typical Electrical and Thermal Characteristics**



**Figure 1 Output Characteristics** 



**Figure 2 Transfer Characteristics** 

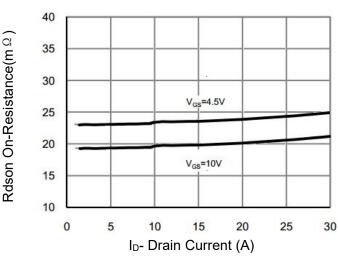


Figure 3 Rdson- Drain Current

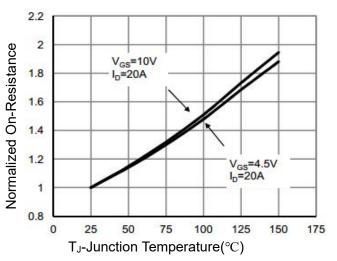


Figure 4 Rdson-Junction Temperature

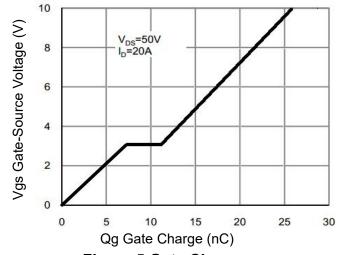


Figure 5 Gate Charge

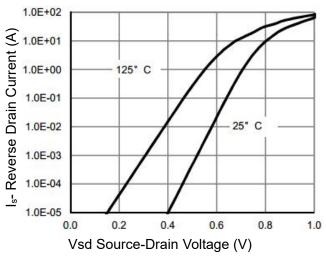


Figure 6 Source- Drain Diode Forward



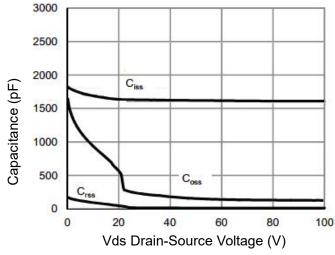


Figure 7 Capacitance vs Vds

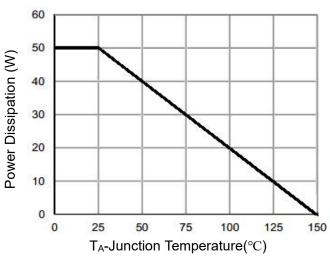


Figure 9 Power De-rating

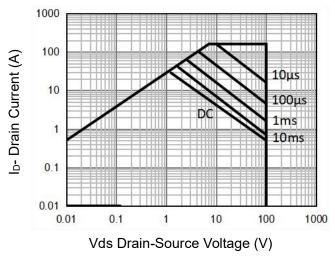


Figure 8 Safe Operation Area (Note 3)

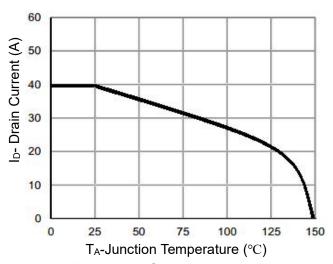


Figure 10 Current De-rating

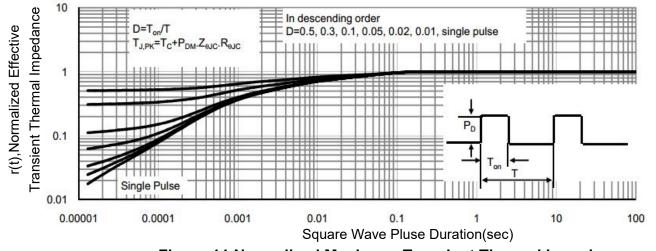
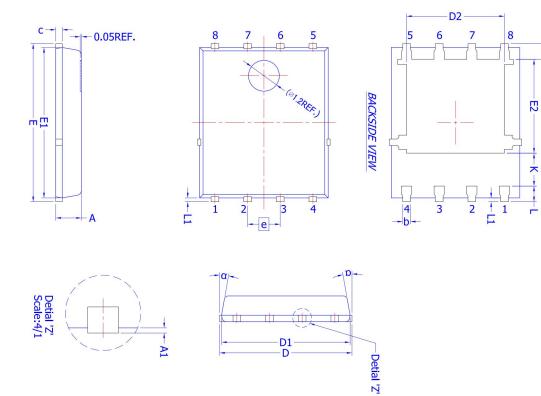


Figure 11 Normalized Maximum Transient Thermal Impedance

## DFN5X6-8L(P) Package Information

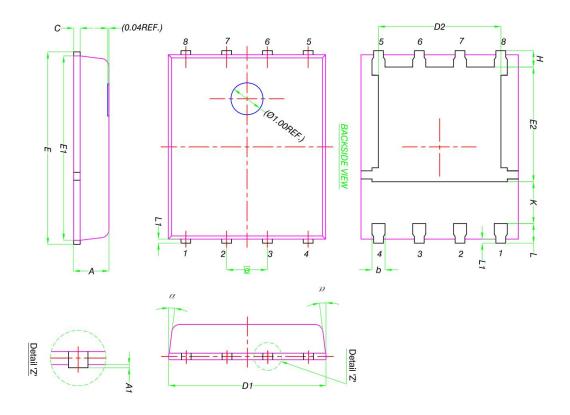


544	MILLIMETERS				
DIM.	MIN.	NOM.	MAX.		
Α	0.90	1.00	1.10		
A1	0	-	0.05		
Ь	0.30	0.40	0.50		
С	0.20	0.25	0.30		
D		5.15 BSC			
D1		5.00 BSC	7		
D2	3.76	3.81	3.86		
E		6.15 BSC	?		
E1	5.80	5.85	5.90		
E2	3.45	3.65	3.85		
e	1.27 BSC				
Н	0.51	0.61	0.71		
K	1.10	-	-		
L	0.51	0.61	0.71		
L1	0.08	0.15	0.23		
α	10°	11°	12°		

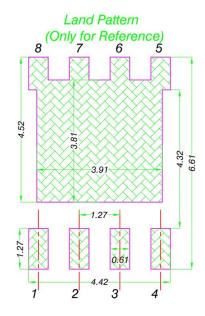
#### Note:

- 1. All Dimension Are In mm;
- Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10mm Per Side.
- 3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic. Body Exclusive Of Mold Flash, Tie Bar, Tie Bar Burrs Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.
- 4. The Package Top May Be Smaller Than The Package Bottom.

## DFN5X6-8L(G) Package Information



DIM.	MILLIMETERS			
	MIN.	NOM.	MAX.	
Α	0.90	1.00	1.10	
A1	0	8	0.05	
b	0.33	0.41	0.51	
С	0.20	0.25	0.30	
D1	4.80	4.90	5.00	
D2	3.61	3.81	3.96	
Ε	5.90	6.00	6.10	
E1	5.70	5.75	5.80	
E2	3.38	3.58	3.78	
е	1.27 BSC			
Н	0.41	0.51	0.61	
K	1.10	-	-	
L	0.51	0.61	0.71	
L1	0.06	0.13	0.20	
α	0°	-	12°	



#### Note:

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- 3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Tie Bar, Tie Bar Burns, Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.

  4. The Package Top May Be Smaller Than The Package Bottom.

## NCEP0140AG

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