

NCE0157T

NCE N-Channel Enhancement Mode Power MOSFET

Description

The NCE0157T uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

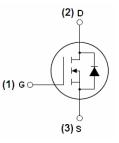
- V_{DS} = 100V, I_{D} =57A $R_{DS(ON)}$ < 16mΩ @ V_{GS} =10V (Typ:11.7mΩ)
- Special process technology for high ESD capability
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation

Application

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply

100% UIS TESTED!

100% AVds TESTED!



Schematic diagram



Marking and pin assignment



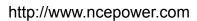
TO-247 top view

Package Marking and Ordering Information

	<u> </u>				
Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE0157T	NCE0157T	TO-247	-	-	-

Absolute Maximum Ratings (T_C=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	100	V
Gate-Source Voltage	V _G S	±20	V
Drain Current-Continuous	I _D	57	Α
Drain Current-Continuous(T _C =100 °C)	I _D (100℃)	40	Α
Pulsed Drain Current	I _{DM}	190	Α
Maximum Power Dissipation	P _D	180	W
Derating factor		1.2	W/℃
Single pulse avalanche energy (Note 5)	E _{AS}	580	mJ
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 175	$^{\circ}$





NCE0157T

Thermal Characteristic

Thermal Resistance, Junction-to-Case (Note 2)	$R_{ heta JC}$	0.83	°C/W
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Electrical Characteristics (T_C=25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	100	110	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =100V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)			•			•
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	V _{DS} =V _{GS} ,I _D =250μA 2		4	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =28A	-	11.7	16	mΩ
Forward Transconductance	g FS	V _{DS} =25V,I _D =28A	32	-	-	S
Dynamic Characteristics (Note4)			•			
Input Capacitance	C _{lss}	\/ 05\/\/ 0\/	-	4400	-	PF
Output Capacitance	C _{oss}	V _{DS} =25V,V _{GS} =0V,	-	320	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.0MHz	-	240	-	PF
Switching Characteristics (Note 4)			•			•
Turn-on Delay Time	t _{d(on)}		-	12	-	nS
Turn-on Rise Time	t _r	V _{DD} =50V,I _D =28A	-	55	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10 V , R_{GEN} =2.5 Ω	-	45	-	nS
Turn-Off Fall Time	t _f		-	47	-	nS
Total Gate Charge	Qg	V 00V/1 00A	-	95	-	nC
Gate-Source Charge	Q _{gs}	V_{DS} =80V, I_{D} =28A, V_{GS} =10V	-	18	-	nC
Gate-Drain Charge	Q_{gd}	V _{GS} =10V	-	25	-	nC
Drain-Source Diode Characteristics			•			•
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =28A	-	0.85	1.2	V
Diode Forward Current (Note 2)	Is		-	-	57	Α
Reverse Recovery Time	t _{rr}	TJ = 25°C, IF = 28A	-	36	-	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs ^(Note3)	-	56	-	nC
		Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD				

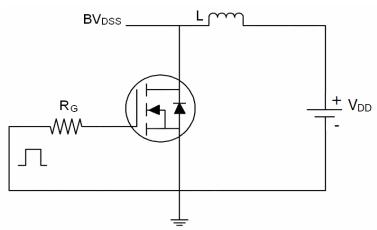
Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- **4.** Guaranteed by design, not subject to production
- 5. EAS condition: Tj=25 $^{\circ}\text{C}$,VDD=50V,VG=10V,L=0.5mH,Rg=25 Ω

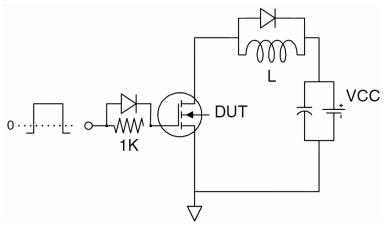


Test Circuit

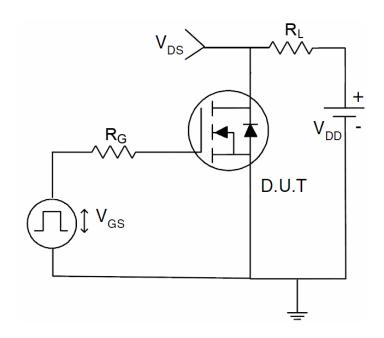
1) E_{AS} test Circuit



2) Gate charge test Circuit



3) Switch Time Test Circuit





Typical Electrical and Thermal Characteristics (Curves)

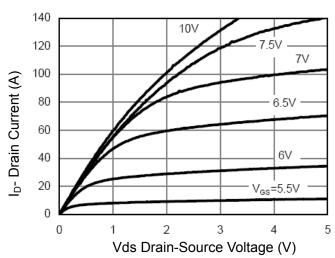


Figure 1 Output Characteristics

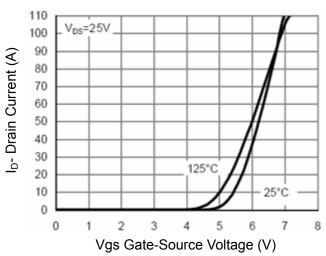


Figure 2 Transfer Characteristics

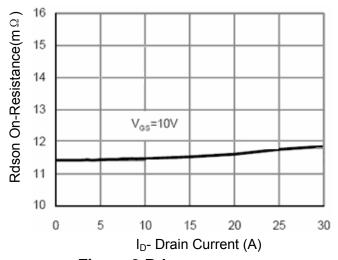


Figure 3 Rdson- Drain Current

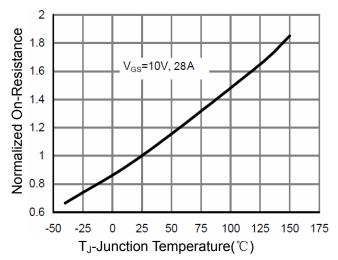


Figure 4 Rdson-JunctionTemperature

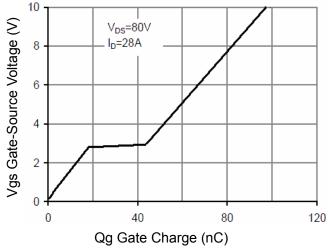


Figure 5 Gate Charge

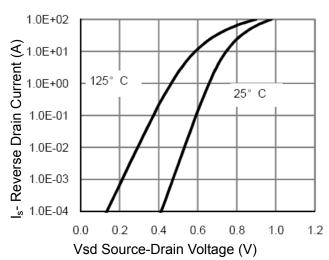


Figure 6 Source- Drain Diode Forward



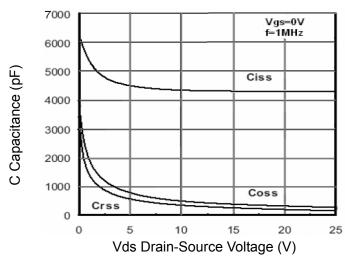


Figure 7 Capacitance vs Vds

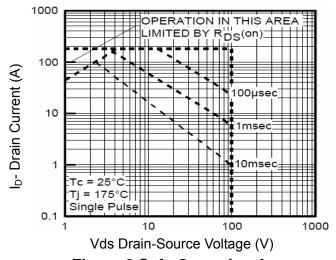


Figure 8 Safe Operation Area

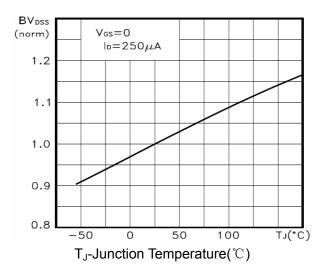


Figure 9 BV_{DSS} vs Junction Temperature

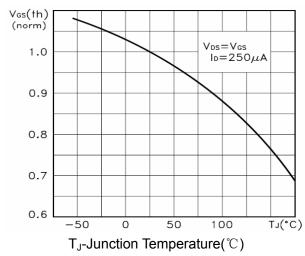


Figure 10 V_{GS(th)} vs Junction Temperature

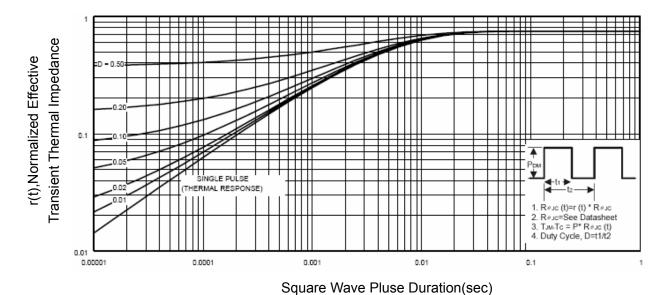


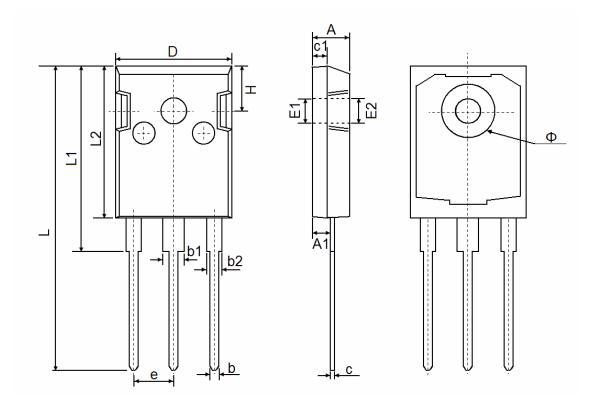
Figure 11 Normalized Maximum Transient Thermal Impedance

Pb Free Product



NCE0157T

TO-247 Package Information



Symbol	Dimensions	In Millimeters	Dimensions In Inches			
Symbol	Min.	Max.	Min.	Max.		
А	4.850	5.150	0.191	0.200		
A1	2.200	2.600	0.087	0.102		
b	1.000	1.400	0.039	0.055		
b1	2.800	3.200	0.110	0.126		
b2	1.800	2.200	0.071	0.087		
С	0.500	0.700	0.020	0.028		
c1	1.900	2.100	0.075	0.083		
D	15.450	15.750	0.608	0.620		
E1	3.500) REF	0.138 REF			
E2	3.600	3.600 REF		0.142 REF		
L	40.900	41.300	1.610	1.626		
L1	24.800	25.100	0.976	0.988		
L2	20.300	20.600	0.799	0.811		
Ф	7.100	7.300	0.280	0.287		
е	5.450) TYP	0.215 TYP			
Н	5.980) REF	0.235 REF			



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