### NCE N-Channel Enhancement Mode Power MOSFET

#### **Description**

The NCE3040Q uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

### **Application**

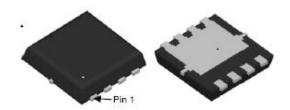
- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

100% UIS TESTED! 100% ΔVds TESTED!

#### **General Features**

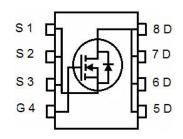
- $V_{DS}$  =30V, $I_D$  =40A  $R_{DS(ON)}$ =5.7mΩ (typical) @  $V_{GS}$ =10V  $R_{DS(ON)}$ =10.0mΩ (typical) @  $V_{GS}$ =4.5V
- High density cell design for ultra low Rdson
- Very low on-resistance R<sub>DS(on)</sub>
- Good stability and uniformity with high E<sub>AS</sub>
- 150 °C operating temperature
- Pb-free lead plating

#### **DFN 3.3X3.3**



**Top View** 

**Bottom View** 



**Schematic Diagram** 

### **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE3040Q	NCE3040Q	DFN 3.3x3.3-8L	Ø330mm	12mm	5000 units

### Absolute Maximum Ratings (T<sub>c</sub>=25℃unless otherwise noted)

• · · · · · · · · · · · · · · · · · · ·	•		
Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	30	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous (Note 1)	I <sub>D</sub>	40	А
Drain Current-Continuous(T <sub>C</sub> =100 °C)	I <sub>D</sub> (100℃)	28.3	А
Pulsed Drain Current	I <sub>DM</sub>	160	А
Maximum Power Dissipation	P <sub>D</sub>	35	W
Derating factor		0.28	W/°C
Single pulse avalanche energy (Note 5)	E <sub>AS</sub>	150	mJ
Operating Junction and Storage Temperature Range	T <sub>J</sub> ,T <sub>STG</sub>	-55 To 150	$^{\circ}$

#### **Thermal Characteristic**

Thermal Resistance,Junction-to-Case <sup>(Note 2)</sup>	R <sub>eJC</sub>	3.6	°C/W



# Electrical Characteristics (TC=25°Cunless otherwise noted)

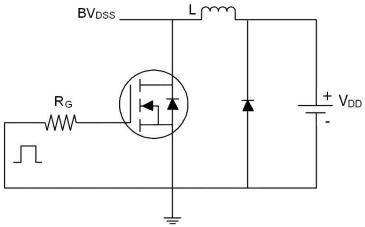
Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics			1	'		
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250µA	30	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =30V,V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS},I_{D}=250\mu A$	1	1.5	2.5	V
Dunin Course On State Besietenes		V <sub>GS</sub> =10V, I <sub>D</sub> =20A	-	5.7	7.0	0
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =20A	-	10	14	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =5V,I <sub>D</sub> =20A	20	-	-	S
Dynamic Characteristics (Note4)	,		1			
Input Capacitance	Clss	\\ 45\\\\ 0\\	-	1650	-	PF
Output Capacitance	Coss	V <sub>DS</sub> =15V,V <sub>GS</sub> =0V,	-	205	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.0MHz	-	177	-	PF
Switching Characteristics (Note 4)			•			
Turn-on Delay Time	t <sub>d(on)</sub>		-	9	-	nS
Turn-on Rise Time	tr	V <sub>DD</sub> =15V,I <sub>D</sub> =20A	-	8	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10 $V$ , $R_{GEN}$ =6 $\Omega$	-	28	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	5	-	nS
Total Gate Charge	Qg	)/ 45\/\ 00A	-	32.3	-	nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =15V,I <sub>D</sub> =20A, V <sub>GS</sub> =10V	-	4.9	-	nC
Gate-Drain Charge	$Q_{\mathrm{gd}}$	VGS-10V	-	6.9	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =20A	-	0.85	1.2	V
Diode Forward Current	Is		-	-	40	Α
Reverse Recovery Time	t <sub>rr</sub>	TJ = 25°C, I <sub>F</sub> = 20A	-	-	27	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs <sup>(Note3)</sup>	-	-	20	nC
Forward Turn-On Time	t <sub>on</sub>	Intrinsic turn-on time is negl	igible (turi	n-on is do	minated b	y LS+LD)

#### Notes:

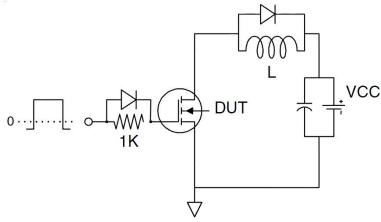
- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board,  $t \le 10$  sec.
- **3.** Pulse Test: Pulse Width ≤  $300\mu$ s, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production
- **5.** EAS condition: Tj=25 $^{\circ}$ C,V<sub>DD</sub>=15V,V<sub>G</sub>=10V,L=0.5mH,Rg=25 $\Omega$

### **Test Circuit**

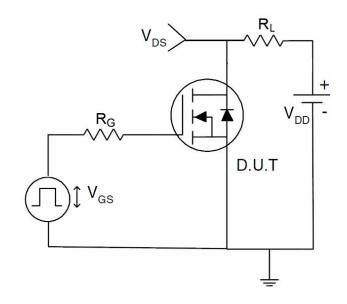
# 1) E<sub>AS</sub> Test Circuits



# 2) Gate Charge Test Circuit



# 3) Switch Time Test Circuit





### **Typical Electrical and Thermal Characteristics (Curves)**

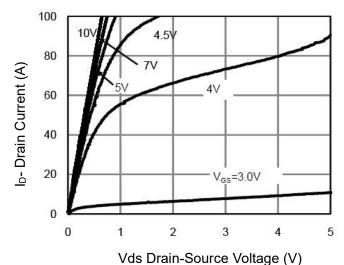
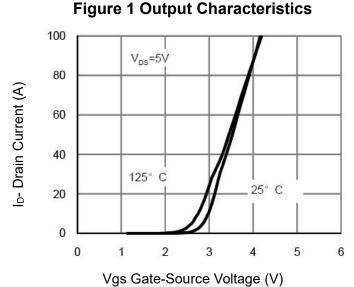


Figure 4 Quitaut Characteristic



**Figure 2 Transfer Characteristics** 

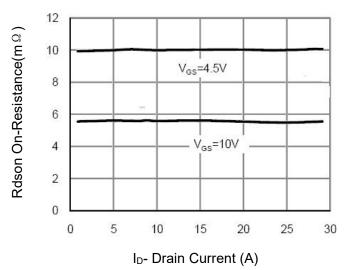


Figure 3 Rdson- Drain Current

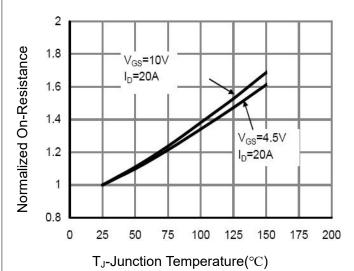


Figure 4 Rdson-JunctionTemperature

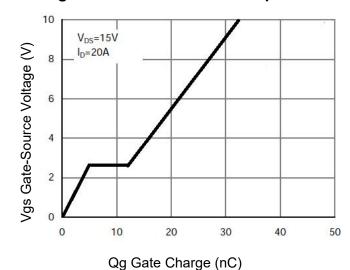


Figure 5 Gate Charge

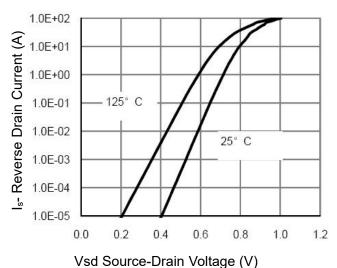


Figure 6 Source- Drain Diode Forward



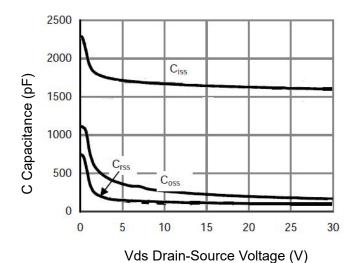


Figure 7 Capacitance vs Vds

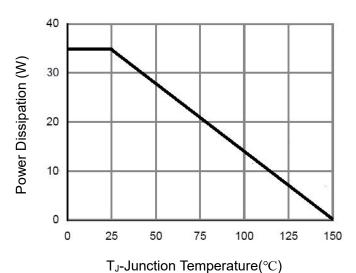


Figure 9 Power De-rating

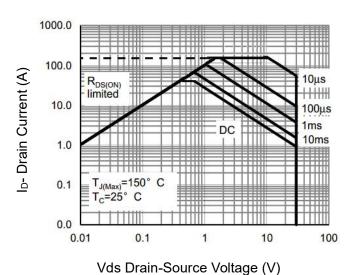
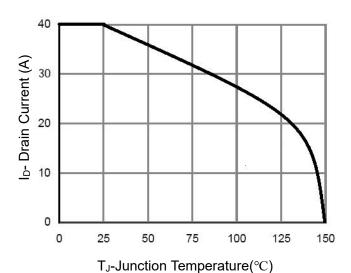
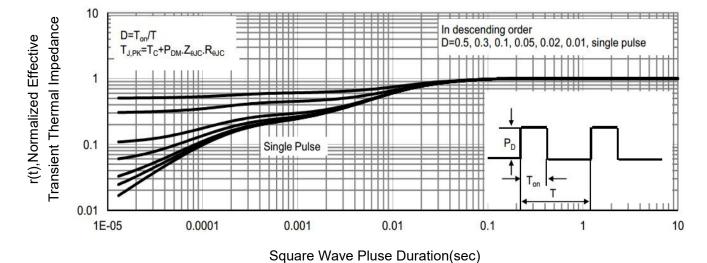


Figure 8 Safe Operation Area

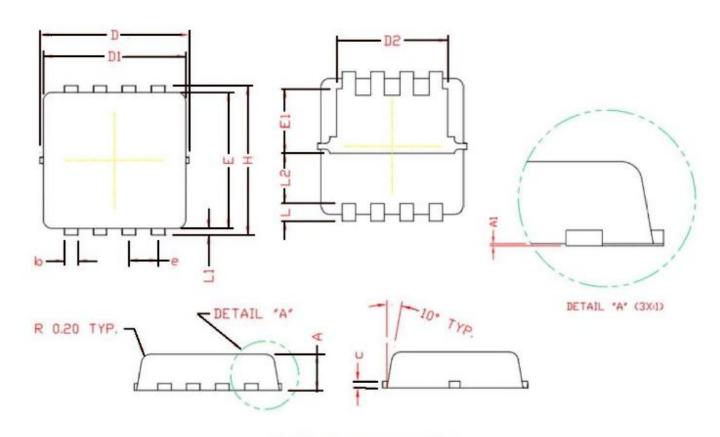


**Figure 10 ID Current- Junction Temperature** 



**Figure 11 Normalized Maximum Transient Thermal Impedance** 

# **DFN3.3X3.3-8L Package Information**



# COMMON DIMENSIONS

(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX	
A	0.70	0.80	0.90	
A1	0.00	0.03	0.05	
b	0.24	0.30	0.35	
c	0.10	0.15	0.20	
D	3. 25	3. 32	3.40	
D1	3.05	3.15	3. 25	
D2	2.40	2.50	2.60	
E	3.00	3.10	3, 20	
E1	1.35	1.45	1.55	
е	0	. 65 BSC		
Н	3. 20	3.30	3.40	
L	0.30	0.40	0.50	
L1	0.10	0.15	0.20	
L2	1. 13 REF.			



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