US79 Series

CMOS Power Hall IC



- Built-in Reverse Voltage Protection
- Built-in RFI Filter
- Power Efficient CMOS and Power MOSFET Drivers allow 400mA without overheating
- Built-in Zener Diodes Protect Outputs
- Eliminate all Fan Components
- Eliminate PC Board
- 5V and 12V Operation
- High Sensitivity for switching symmetry
- Locked Rotor Shutdown

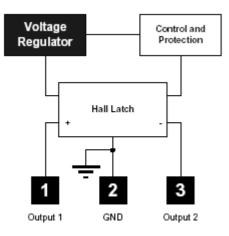
3. Ordering information

2. Applications

- Fan sizes up to 90mm
- Current range up to 400mA

Product Code	Temperature Code	Package Code	Option Code	Packing Form Code
US79	K (-40°C to 125°C)	UA (TO-92 flat)	AAA-000	BU

4. Functional Diagram



5. Description

The US79KUA is the most advanced Smart Fan

Control Hall IC. It is designed for 5V and 12V cooling commutation. The chip contains many features to allow survival in a harsh environment. The IC was designed to eliminate all discrete components such as capacitors, resistors, transistors, diodes, PC board and associated labor, replacing US\$0.25 to US\$0.35 in direct cost.

The K rating guarantees proper operation up to an ambient temperature of 125°C. Hall IC circuitry and power FET output provide a low power dissipation cool chip.

Locked Rotor conditions are detected by the IC when there is no motion for one second and will shut off the motor drive for five seconds. Then, the IC will turn on the drive current for one second. This sequence continues indefinitely until the locked rotor condition is fixed. This feature prevents overheating.





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6. Glossary of Terms

MilliTesla (mT), Gauss: Units of magnetic flux density; 1 milliTesla = 10 Gauss. Two-Coil Fan: a fan with two coil windings, current alternates from 1 coil to the other depending on the polarity of the magnetic field.

Two-wire Fan: A fan that has only two connections for the power supply plus and minus. Locked rotor: The condition of a fan that has stopped spinning due to mechanical blockage .

7. Absolute Maximum Ratings

Supply Voltage, V _{DD}	(-0.3 to 18)V
Output Current (Fault), IOUT	500mA
Operating Temperature Range, TA	-40 to 125°C
Storage Temperature Range, TS	-55 to 165°C
Maximun Junction Temp, TJ	150°C

Exceeding the absolute maximum ratings may cause permanent damage. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

8. US79 Electrical Specifications

DC operating parameters: TA = 25 $^{\circ}$ C, VDD = 12V unless otherwise specified

Parameter	Symbol	Test Conditions	Min	Тур	Max	Units
Supply Voltage	Vdd	Operating	3.5	-	18	V
Supply Current	Idd	Operating		1.4	2.0	mA
Output Saturation voltage	Vdss	Ιουτ = 150mA		300	600	mV
Output Saturation voltage	Vdss	Ιουτ = 350mA		650	1100	mV
Thermal resistance	Rth	Operating		190		°C/W
Locked-Rotor Period	t _{on}			0.8		S
Locked-Rotor period	t _{off}			5		S



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9. US79 Magnetic Specifications

DC operating parameters: $T_A = 25 \circ C$, $V_{DD} = 12V$ unless otherwise specified.

Parameter	Symbol	Test Conditions	Min	Тур	Max	Units
Operate Point	Вор	Operating		2.5	6.0	mT
Release Point	Brp	Operating	-6.0	-2.5		mT
Hysteresis	Внуз	Operating	2.0	5.0	-	mT

10. Unique Features

Reverse voltage protection eliminates the need for a diode. Reverse current flows through the coils and the chip. Power dissipation is (2 * Istall/Istart * 0.7V).

Table 1 presents max temperature for each current.

Istall/ Istart	T _A Maximum Rev V Test
100mA	125°C
200mA	100°C
300mA	70° C
400mA	40° C

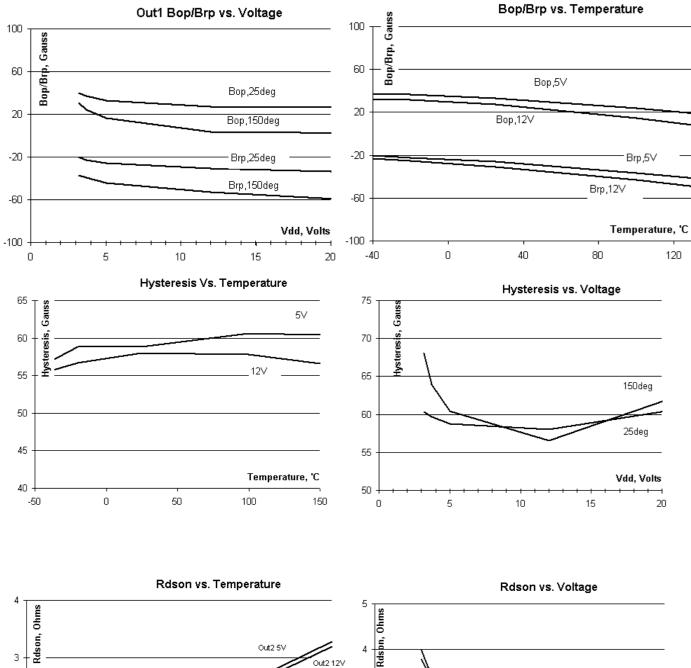
Reverse Voltage protection is provided by the motor windings.

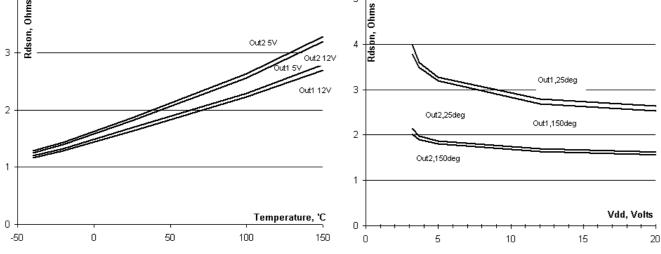
The 35V Zener diodes clamp the output drivers for overstress protection.

US79 Series

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11. Performance Graphs



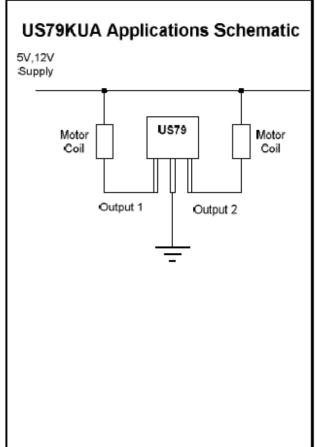


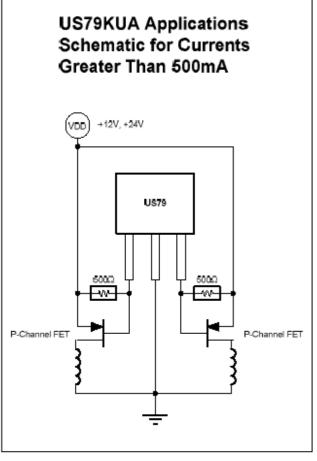
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12. Applications Information









CMOS Power Hall IC



13. Standard information regarding manufacturability of Melexis products with different soldering processes

Our products are classified and qualified regarding soldering technology, solderability and moisture sensitivity level according to following test methods:

Reflow Soldering SMD's (Surface Mount Devices)

- IPC/JEDEC J-STD-020 Moisture/Reflow Sensitivity Classification for Nonhermetic Solid State Surface Mount Devices (classification reflow profiles according to table 5-2)
- EIA/JEDEC JESD22-A113 Preconditioning of Nonhermetic Surface Mount Devices Prior to Reliability Testing (reflow profiles according to table 2)

Wave Soldering SMD's (Surface Mount Devices) and THD's (Through Hole Devices)

- EN60749-20 Resistance of plastic- encapsulated SMD's to combined effect of moisture and soldering heat
- EIA/JEDEC JESD22-B106 and EN60749-15 Resistance to soldering temperature for through-hole mounted devices

Iron Soldering THD's (Through Hole Devices)

• EN60749-15 Resistance to soldering temperature for through-hole mounted devices

Solderability SMD's (Surface Mount Devices) and THD's (Through Hole Devices)

• EIA/JEDEC JESD22-B102 and EN60749-21 Solderability

For all soldering technologies deviating from above mentioned standard conditions (regarding peak temperature, temperature gradient, temperature profile etc) additional classification and qualification tests have to be agreed upon with Melexis.

The application of Wave Soldering for SMD's is allowed only after consulting Melexis regarding assurance of adhesive strength between device and board.

Melexis recommends reviewing on our web site the General Guidelines <u>soldering recommendation</u> (www.melexis.com/soldering-and-welding) as well as <u>trim&form recommendations</u> (www.melexis.com/lead-forming)

Melexis is contributing to global environmental conservation by promoting **lead free** solutions. For more information on qualifications of **RoHS** compliant products (RoHS = European directive on the Restriction Of the use of certain Hazardous Substances) please visit the quality page on our website: <u>https://www.melexis.com/environmental-forms-and-declarations</u>

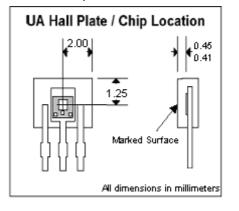
14. ESD Precautions

Electronic semiconductor products are sensitive to Electro Static Discharge (ESD). Always observe Electro Static Discharge control procedures whenever handling semiconductor products.

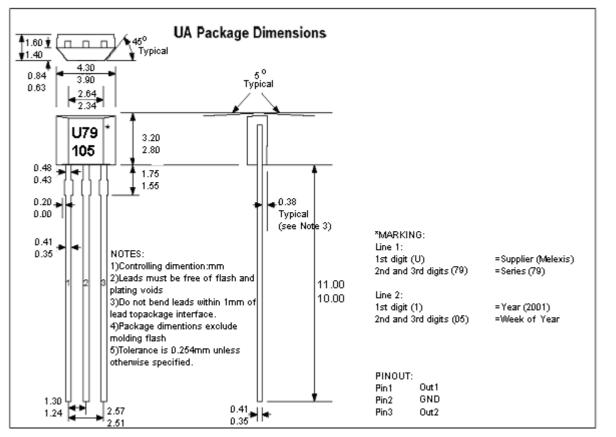


15. UA Package Information

15.1. Hall plate location



15.2. AAA000 Package variant



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16. Revision history

Rev	Date	Author	Comment
009	6-Jun-17	DLM	 New template Reduced IDD MAX specification to 2.0mA Updated solderability guidelines
010	15-Sep-21	CEL	 Removed the variant US79KUA-AAA-001-BU Chapter 13: links updated Contact section updated Disclaimer updated

17. Contact

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