

## 20MHz, High Slew Rate, Uncompensated, High Input Impedance, Operational Amplifiers

HA-2520, HA-2522, HA-2525 comprise a series of operational amplifiers delivering an unsurpassed combination of specifications for slew rate, bandwidth and settling time. These dielectrically isolated amplifiers are controlled at close loop gains greater than 3 without external compensation. In addition, these high performance components also provide low offset current and high input impedance.

120V/ms slew rate and 200ns (0.2%) settling time of these amplifiers make them ideal components for pulse amplification and data acquisition designs. These devices are valuable components for RF and video circuitry requiring up to 20MHz gain bandwidth and 2MHz power bandwidth. For accurate signal conditioning designs the HA-2520, HA-2522, HA-2525's superior dynamic specifications are complemented by 10nA offset current, 100M $\Omega$  input impedance and offset trim capability.

### Ordering Information

| PART NUMBER        | PART MARKING | TEMP. RANGE (°C) | PACKAGE              | PKG. DWG. # |
|--------------------|--------------|------------------|----------------------|-------------|
| HA2-2520-2         | HA2-2520-2   | -55 to +125      | 8 Ld Metal Can       | T8.C        |
| HA7-2520-2         | HA7-2520-2   | -55 to +125      | 8 Ld CerDIP          | F8.3A       |
| HA2-2522-2         | HA2-2522-2   | -55 to +125      | 8 Ld Metal Can       | T8.C        |
| HA2-2525-5         | HA2-2525-5   | 0 to +75         | 8 Ld Metal Can       | T8.C        |
| HA3-2525-5         | HA3-2525-5   | 0 to +75         | 8 Ld PDIP            | E8.3        |
| HA3-2525-5Z (Note) | HA3-2525-5Z  | 0 to +75         | 8 Ld PDIP* (Pb-free) | E8.3        |
| HA7-2525-5         | HA7-2525-5   | 0 to +75         | 8 Ld CerDIP          | F8.3A       |
| HA9P2525-5         | 25255        | 0 to +75         | 8 Ld SOIC            | M8.15       |
| HA9P2525-5Z (Note) | 2525-5Z      | 0 to +75         | 8 Ld SOIC (Pb-free)  | M8.15       |

NOTE: Intersil Pb-free plus anneal products employ special Pb-free material sets; molding compounds/die attach materials and 100% matte tin plate termination finish, which are RoHS compliant and compatible with both SnPb and Pb-free soldering operations. Intersil Pb-free products are MSL classified at Pb-free peak reflow temperatures that meet or exceed the Pb-free requirements of IPC/JEDEC J STD-020.

\*Pb-free PDIPs can be used for through hole wave solder processing only. They are not intended for use in Reflow solder processing applications.

### Features

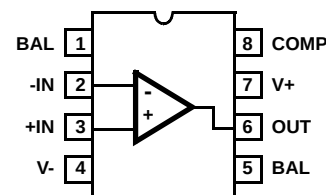
- High Slew Rate . . . . . 120V/ $\mu$ s
- Fast Settling . . . . . 200ns
- Full Power Bandwidth . . . . . 2MHz
- Gain Bandwidth ( $A_V \geq 3$ ) . . . . . 20MHz
- High Input Impedance . . . . . 100M $\Omega$
- Low Offset Current . . . . . 10nA
- Compensation Pin for Unity Gain Capability
- Pb-Free Plus Anneal Available (RoHS Compliant)

### Applications

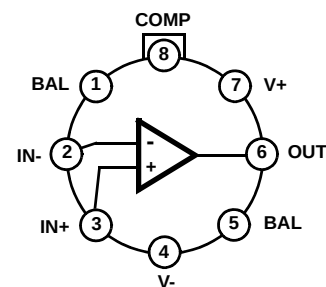
- Data Acquisition Systems
- RF Amplifiers
- Video Amplifiers
- Signal Generators

### Pinouts

**HA-2520, HA-2525**  
(8 LD Cerdip, 8 LD PDIP, Cerdip, SOIC)  
TOP VIEW



**HA-2520, HA-2522, HA-2525**  
(8 LD METAL CAN)  
TOP VIEW



# HA-2520, HA-2522, HA-2525

## Absolute Maximum Ratings

Supply Voltage (Between V+ and V- Terminals) . . . . . 40V  
 Differential Input Voltage . . . . . 15V  
 Output Current . . . . . 50mA

### Operating Conditions

Temperature Range  
 HA-2520/2522-2 . . . . . -55°C to +125°C  
 HA-2525-5 . . . . . 0°C to +75°C

## Thermal Information

Thermal Resistance (Typical, Note 1)  $\theta_{JA}$  (°C/W)  $\theta_{JC}$  (°C/W)  
 Metal Can Package . . . . . 165 80  
 PDIP Package\* . . . . . 96 N/A  
 CERDIP Package . . . . . 135 50  
 SOIC Package . . . . . 157 N/A

Maximum Junction Temperature (Hermetic Packages) . . . . . +175°C  
 Maximum Junction Temperature (Plastic Package) . . . . . +150°C  
 Maximum Storage Temperature Range . . . . . -65°C to +150°C  
 Maximum Lead Temperature (Soldering 10s) . . . . . +300°C

\*Pb-free PDIPs can be used for through hole wave solder processing only. They are not intended for use in Reflow solder processing applications.

**CAUTION:** Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

### NOTE:

1.  $\theta_{JA}$  is measured with the component mounted on an evaluation PC board in free air.

## Electrical Specifications $V_{SUPPLY} = \pm 15V$

| PARAMETER                                 | TEMP<br>(°C) | HA-2520-2 |       |     | HA-2522-2 |       |     | HA-2525-5 |       |     | UNITS |
|-------------------------------------------|--------------|-----------|-------|-----|-----------|-------|-----|-----------|-------|-----|-------|
|                                           |              | MIN       | TYP   | MAX | MIN       | TYP   | MAX | MIN       | TYP   | MAX |       |
| INPUT CHARACTERISTICS                     |              |           |       |     |           |       |     |           |       |     |       |
| Offset Voltage                            | 25           | -         | 4     | 8   | -         | 5     | 10  | -         | 5     | 10  | mV    |
|                                           | Full         | -         | -     | 11  | -         | -     | 14  | -         | -     | 14  | mV    |
| Offset Voltage Drift                      | Full         | -         | 20    | -   | -         | 25    | -   | -         | 30    | -   | mV/°C |
| Bias Current                              | 25           | -         | 100   | 200 | -         | 125   | 250 | -         | 125   | 250 | nA    |
|                                           | Full         | -         | -     | 400 | -         | -     | 500 | -         | -     | 500 | nA    |
| Offset Current                            | 25           | -         | 10    | 25  | -         | 20    | 50  | -         | 20    | 50  | nA    |
|                                           | Full         | -         | -     | 50  | -         | -     | 100 | -         | -     | 100 | nA    |
| Input Resistance (Note 2)                 | 25           | 50        | 100   | -   | 40        | 100   | -   | 40        | 100   | -   | MW    |
| Common Mode Range                         | Full         | ±10.0     | -     | -   | ±10.0     | -     | -   | ±10.0     | -     | -   | V     |
| TRANSFER CHARACTERISTICS                  |              |           |       |     |           |       |     |           |       |     |       |
| Large Signal Voltage Gain<br>(Notes 3, 6) | 25           | 10        | 15    | -   | 7.5       | 15    | -   | 7.5       | 15    | -   | kV/V  |
|                                           | Full         | 7.5       | -     | -   | 5         | -     | --  | 5         | -     | -   | kV/V  |
| Common Mode Rejection Ratio<br>(Note 4)   | Full         | 80        | 90    | -   | 74        | 90    | -   | 74        | 90    | -   | dB    |
| Gain Bandwidth (Notes 2, 5)               | 25           | 10        | 20    | -   | 10        | 20    | -   | 10        | 20    | -   | MHz   |
| Minimum Stable Gain                       | 25           | 3         | -     | -   | 3         | -     | -   | 3         | -     | -   | V/V   |
| OUTPUT CHARACTERISTICS                    |              |           |       |     |           |       |     |           |       |     |       |
| Output Voltage Swing (Note 3)             | Full         | ±10.0     | ±12.0 | -   | ±10.0     | ±12.0 | -   | ±10.0     | ±12.0 | -   | V     |
| Output Current (Note 6)                   | 25           | ±10       | ±20   | -   | ±10       | ±20   | -   | ±10       | ±20   | -   | mA    |
| Full Power Bandwidth<br>(Notes 6, 11)     | 25           | 1.5       | 2.0   | -   | 1.2       | 2.0   | -   | 1.2       | 2.0   | -   | MHz   |
| TRANSIENT RESPONSE (A <sub>V</sub> = +3)  |              |           |       |     |           |       |     |           |       |     |       |
| Rise Time (Notes 3, 7, 8, 10)             | 25           | -         | 25    | 50  | -         | 25    | 50  | -         | 25    | 50  | ns    |
| Overshoot (Notes 3, 7, 8, 10)             | 25           | -         | 25    | 40  | -         | 25    | 50  | -         | 25    | 50  | %     |
| Slew Rate (Notes 3, 7, 10, 12)            | 25           | ±100      | ±120  | -   | ±80       | ±120  | -   | ±80       | ±120  | -   | V/μs  |
| Settling Time (Notes 3, 7, 10, 12)        | 25           | -         | 0.20  | -   | -         | 0.20  | -   | -         | 0.20  | -   | μs    |

**Electrical Specifications**  $V_{\text{SUPPLY}} = \pm 15\text{V}$  (Continued)

| PARAMETER                             | TEMP<br>(°C) | HA-2520-2 |     |     | HA-2522-2 |     |     | HA-2525-5 |     |     | UNITS |
|---------------------------------------|--------------|-----------|-----|-----|-----------|-----|-----|-----------|-----|-----|-------|
|                                       |              | MIN       | TYP | MAX | MIN       | TYP | MAX | MIN       | TYP | MAX |       |
| POWER SUPPLY CHARACTERISTICS          |              |           |     |     |           |     |     |           |     |     |       |
| Supply Current                        | 25           | -         | 4   | 6   | -         | 4   | 6   | -         | 4   | 6   | mA    |
| Power Supply Rejection Ratio (Note 9) | Full         | 80        | 90  | -   | 74        | 90  | -   | 74        | 90  | -   | dB    |

**NOTES:**

2. This parameter value is based on design calculations.
3.  $R_L = 2\text{k}\Omega$ .
4.  $V_{\text{CM}} = \pm 10\text{V}$ .
5.  $A_V > 10$ .
6.  $V_O = \pm 10.0\text{V}$ .
7.  $C_L = 50\text{pF}$ .
8.  $V_O = \pm 200\text{mV}$ .
9.  $DV = \pm 5.0\text{V}$ .
10. See Transient Response Test Circuits and Waveforms.
11. Full Power Bandwidth guaranteed based on slew rate measurement using:  $\text{FPBW} = \frac{\text{Slew Rate}}{2\pi V_{\text{PEAK}}}$ .
12.  $V_{\text{OUT}} = \pm 5\text{V}$ .

**Test Circuits and Waveforms**

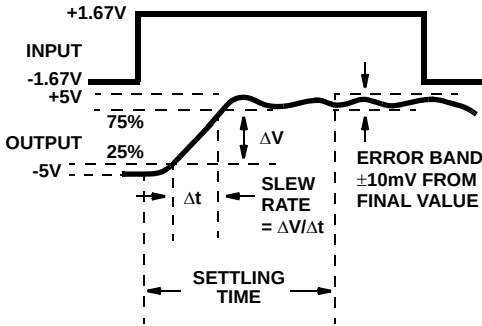
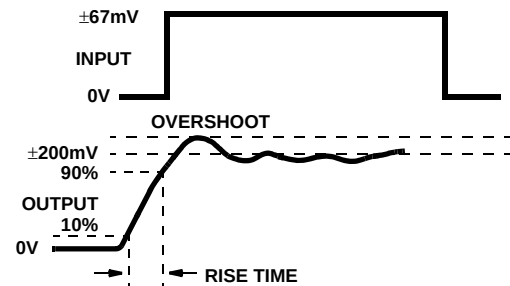


FIGURE 1. SLEW RATE AND SETTLING TIME



NOTE: Measured on both positive and negative transitions from 0V to +200mV and 0V to -200mV at the output.

FIGURE 2. TRANSIENT RESPONSE

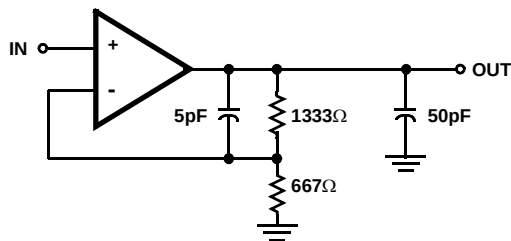
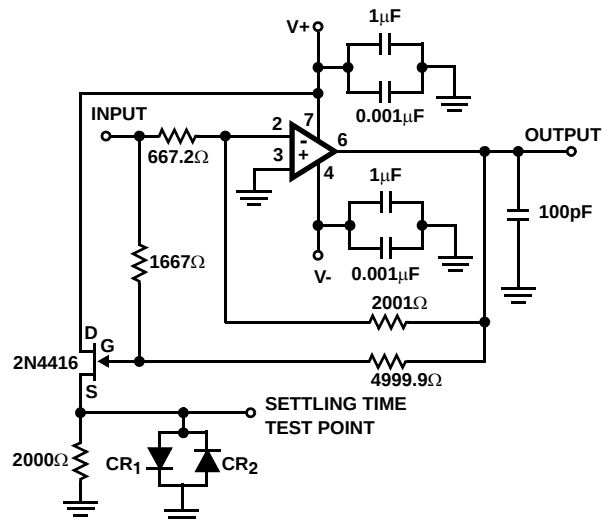


FIGURE 3. SLEW RATE AND TRANSIENT RESPONSE



**NOTES:**

13.  $A_V = -3$ .
14. Feedback and summing resistor ratios should be 0.1% matched.
15. Clipping diodes  $CR_1$  and  $CR_2$  are optional. HP5082-2810 recommended.

FIGURE 4. SETTLING TIME TEST CIRCUIT



## Typical Application

### Inverting Unity Gain Circuit

Figure 6 shows a Compensation Circuit for an inverting unity gain amplifier. The circuit was tested for functionality with supply voltages from  $\pm 4\text{V}$  to  $\pm 15\text{V}$ , and the performance as tested was: Slew Rate  $\approx 120\text{V}/\mu\text{s}$ ; Bandwidth  $\approx 10\text{MHz}$ ; and Settling Time (0.1%)  $\approx 500\text{ns}$ . Figure 7 illustrates the amplifier's frequency response, and it is important to note that capacitance at pin 8 must be minimized for maximum bandwidth.

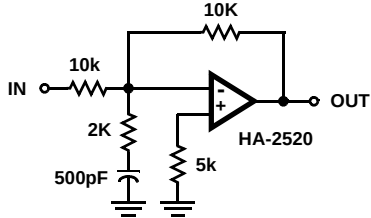


FIGURE 6. INVERTING UNITY GAIN CIRCUIT

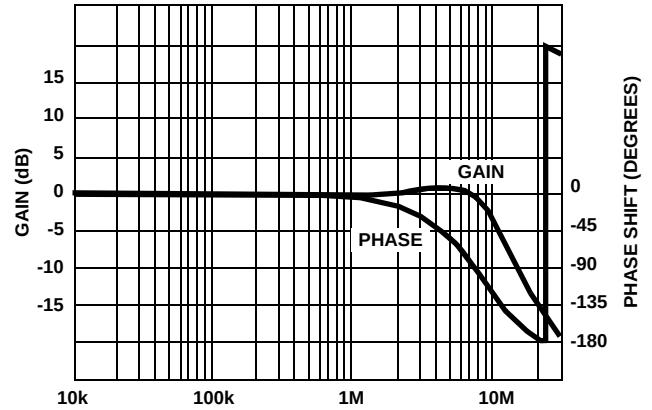


FIGURE 7. FREQUENCY RESPONSE FOR INVERTING UNITY GAIN CIRCUIT

## Typical Performance Curves $V_S = \pm 15\text{V}$ , $T_A = 25^\circ\text{C}$ , Unless Otherwise Specified

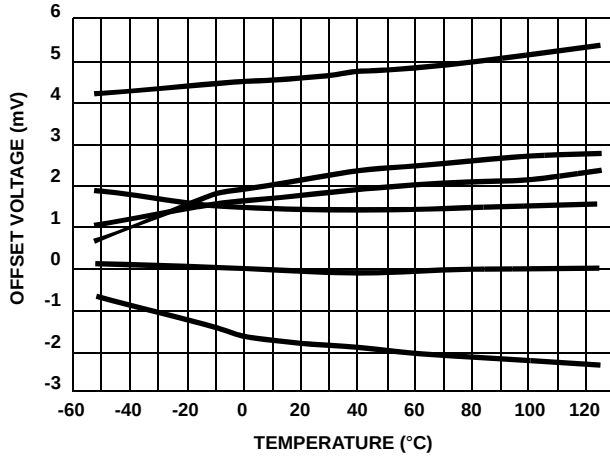


FIGURE 8. OFFSET VOLTAGE vs TEMPERATURE (6 TYPICAL UNITS FROM 3 LOTS)

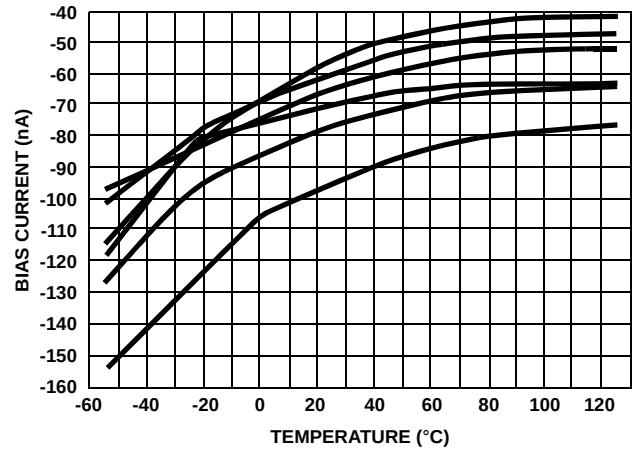


FIGURE 9. BIAS CURRENT vs TEMPERATURE (6 TYPICAL UNITS FROM 3 LOTS)

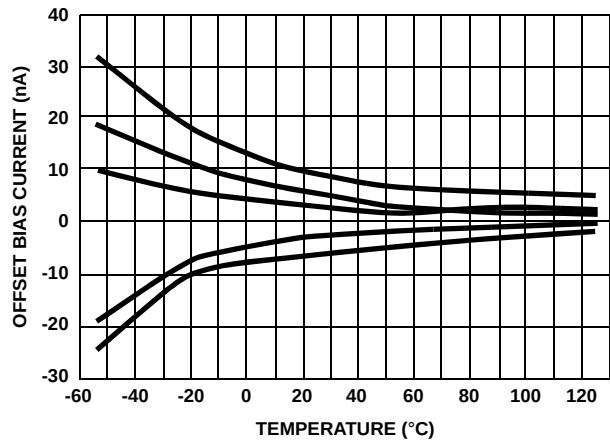


FIGURE 10. OFFSET CURRENT vs TEMPERATURE (5 TYPICAL UNITS FROM 3 LOTS)

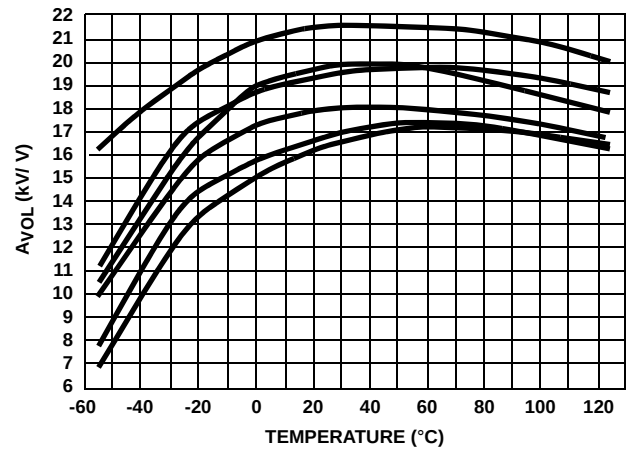


FIGURE 11. OPEN LOOP GAIN vs TEMPERATURE (6 TYPICAL UNITS FROM 3 LOTS)

**Typical Performance Curves**  $V_S = \pm 15V$ ,  $T_A = 25^\circ C$ , Unless Otherwise Specified (Continued)

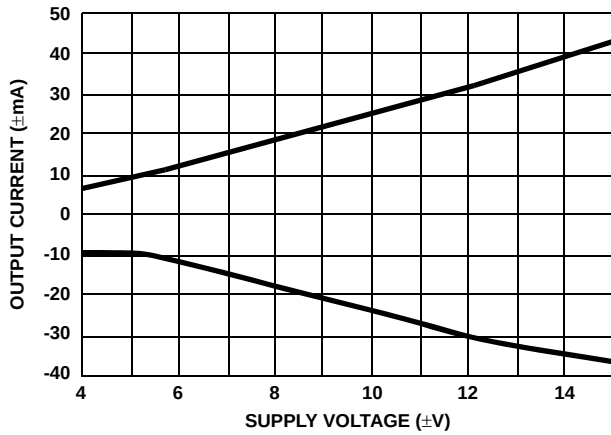


FIGURE 12. OUTPUT CURRENT vs SUPPLY VOLTAGE

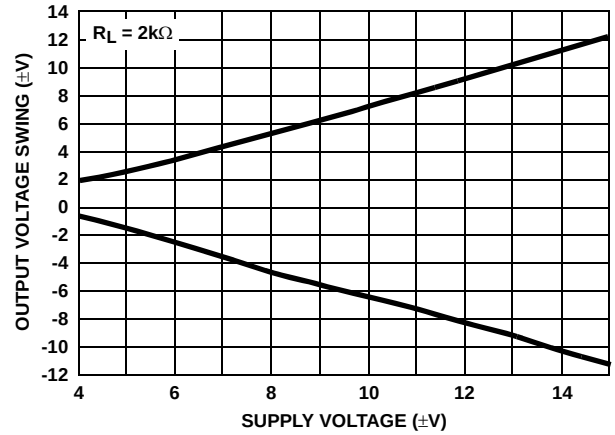


FIGURE 13. OUTPUT VOLTAGE SWING vs SUPPLY VOLTAGE

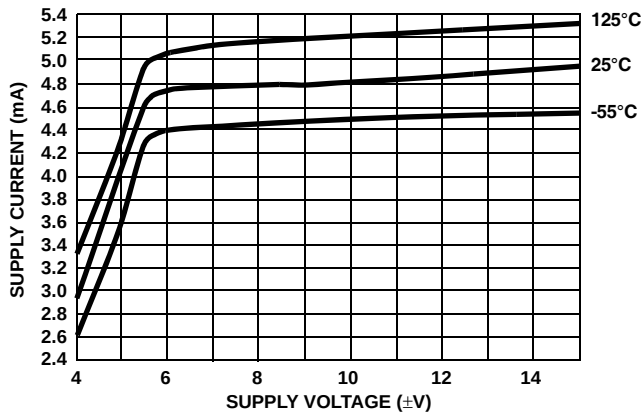


FIGURE 14. SUPPLY CURRENT vs SUPPLY VOLTAGE

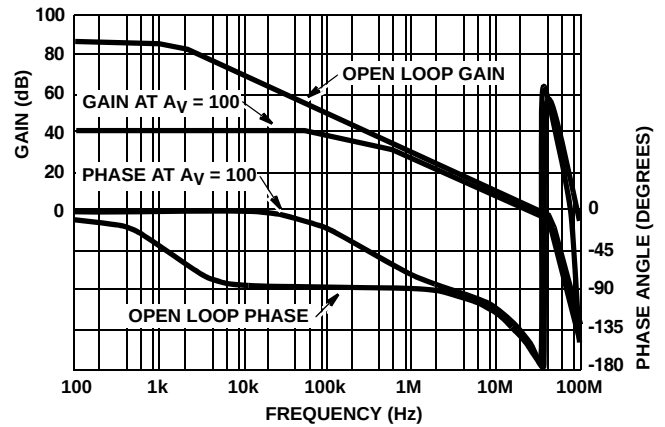


FIGURE 15. FREQUENCY RESPONSE

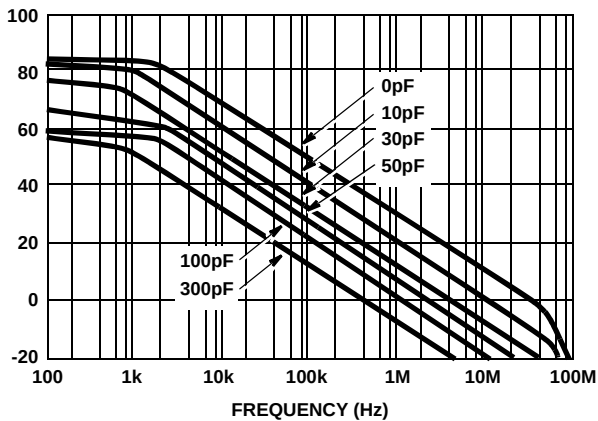


FIGURE 16. OPEN LOOP FREQUENCY RESPONSE FOR VARIOUS VALUES OF CAPACITORS FROM COMP PIN TO GROUND

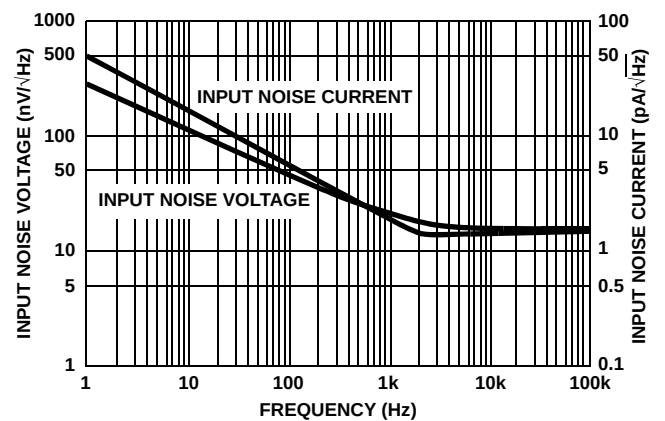


FIGURE 17. INPUT NOISE CHARACTERISTICS

**Typical Performance Curves**  $V_S = \pm 15V$ ,  $T_A = 25^\circ C$ , Unless Otherwise Specified (Continued)

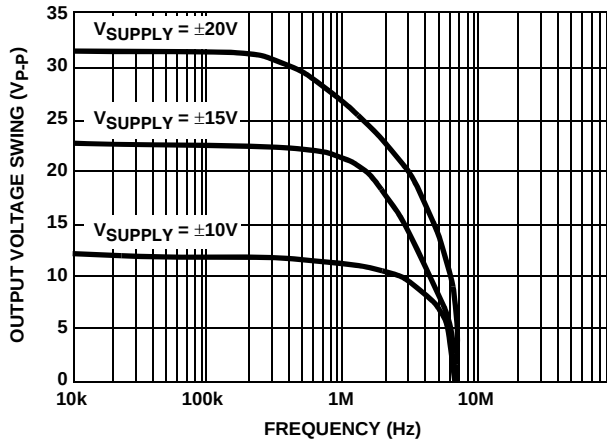


FIGURE 18. OUTPUT VOLTAGE SWING vs FREQUENCY

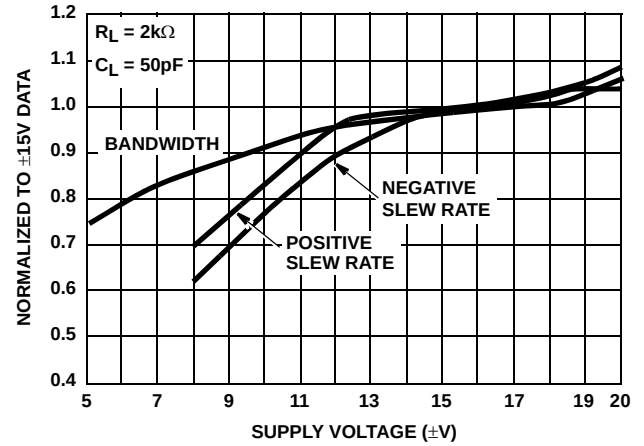


FIGURE 19. NORMALIZED AC PARAMETERS vs SUPPLY VOLTAGE

**Die Characteristics**

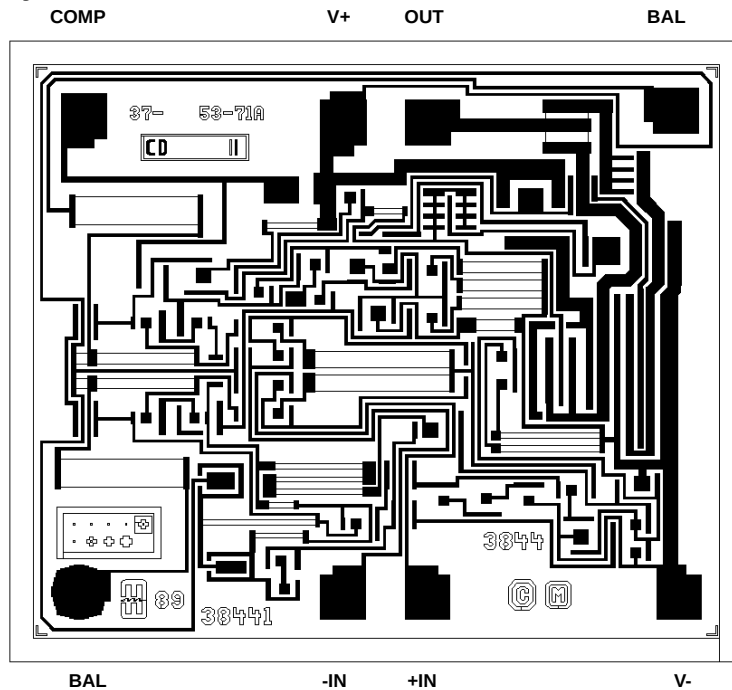
**SUBSTRATE POTENTIAL:**  
Unbiased

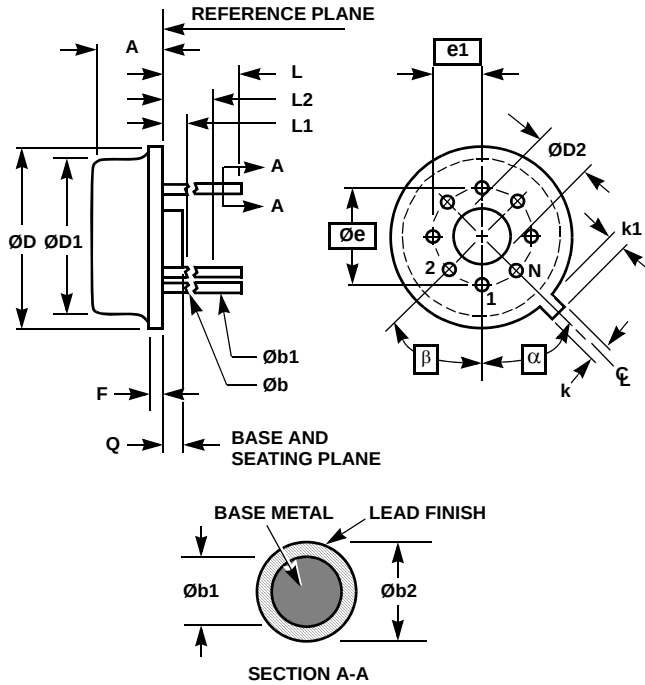
**PROCESS:**  
Bipolar Dielectric Isolation

**TRANSISTOR COUNT:**  
40

**Metallization Mask Layout**

HA-2520, HA-2522, HA-2525



**Metal Can Packages (Can)****NOTES:**

1. (All leads) Øb applies between L1 and L2. Øb1 applies between L2 and 0.500 from the reference plane. Diameter is uncontrolled in L1 and beyond 0.500 from the reference plane.
2. Measured from maximum diameter of the product.
3. a is the basic spacing from the centerline of the tab to terminal 1 and b is the basic spacing of each lead or lead position (N -1 places) from a, looking at the bottom of the package.
4. N is the maximum number of terminal positions.
5. Dimensioning and tolerancing per ANSI Y14.5M - 1982.
6. Controlling dimension: INCH.

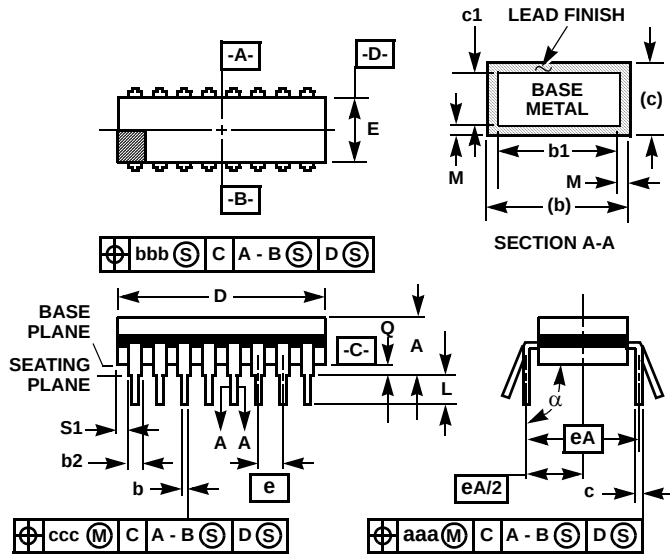
**T8.C MIL-STD-1835 MACY1-X8 (A1)  
8 LEAD METAL CAN PACKAGE**

| SYMBOL | INCHES    |       | MILLIMETERS |       | NOTES |
|--------|-----------|-------|-------------|-------|-------|
|        | MIN       | MAX   | MIN         | MAX   |       |
| A      | 0.165     | 0.185 | 4.19        | 4.70  | -     |
| Øb     | 0.016     | 0.019 | 0.41        | 0.48  | 1     |
| Øb1    | 0.016     | 0.021 | 0.41        | 0.53  | 1     |
| Øb2    | 0.016     | 0.024 | 0.41        | 0.61  | -     |
| ØD     | 0.335     | 0.375 | 8.51        | 9.40  | -     |
| ØD1    | 0.305     | 0.335 | 7.75        | 8.51  | -     |
| ØD2    | 0.110     | 0.160 | 2.79        | 4.06  | -     |
| e      | 0.200 BSC |       | 5.08 BSC    |       | -     |
| e1     | 0.100 BSC |       | 2.54 BSC    |       | -     |
| F      | -         | 0.040 | -           | 1.02  | -     |
| k      | 0.027     | 0.034 | 0.69        | 0.86  | -     |
| k1     | 0.027     | 0.045 | 0.69        | 1.14  | 2     |
| L      | 0.500     | 0.750 | 12.70       | 19.05 | 1     |
| L1     | -         | 0.050 | -           | 1.27  | 1     |
| L2     | 0.250     | -     | 6.35        | -     | 1     |
| Q      | 0.010     | 0.045 | 0.25        | 1.14  | -     |
| a      | 45° BSC   |       | 45° BSC     |       | 3     |
| b      | 45° BSC   |       | 45° BSC     |       | 3     |
| N      | 8         |       | 8           |       | 4     |

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## Ceramic Dual-In-Line Frit Seal Packages (CERDIP)



## NOTES:

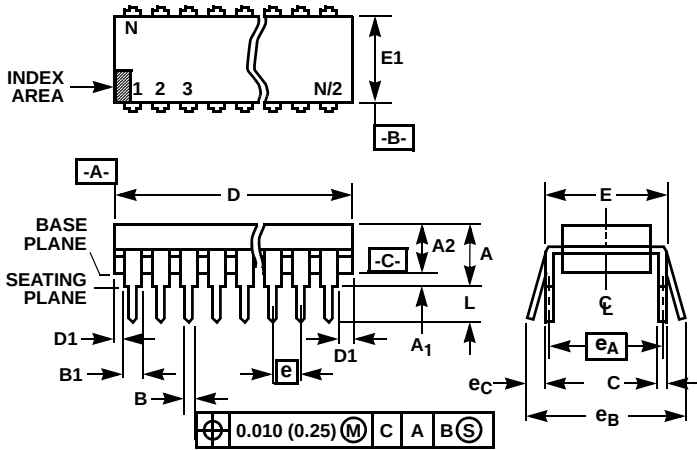
1. Index area: A notch or a pin one identification mark shall be located adjacent to pin one and shall be located within the shaded area shown. The manufacturer's identification shall not be used as a pin one identification mark.
2. The maximum limits of lead dimensions b and c or M shall be measured at the centroid of the finished lead surfaces, when solder dip or tin plate lead finish is applied.
3. Dimensions b1 and c1 apply to lead base metal only. Dimension M applies to lead plating and finish thickness.
4. Corner leads (1, N, N/2, and N/2+1) may be configured with a partial lead paddle. For this configuration dimension b3 replaces dimension b2.
5. This dimension allows for off-center lid, meniscus, and glass overrun.
6. Dimension Q shall be measured from the seating plane to the base plane.
7. Measure dimension S1 at all four corners.
8. N is the maximum number of terminal positions.
9. Dimensioning and tolerancing per ANSI Y14.5M - 1982.
10. Controlling dimension: INCH

F8.3A MIL-STD-1835 GDIP1-T8 (D-4, CONFIGURATION A)  
8 LEAD CERAMIC DUAL-IN-LINE FRIT SEAL PACKAGE

| SYMBOL   | INCHES    |        | MILLIMETERS |       | NOTES |
|----------|-----------|--------|-------------|-------|-------|
|          | MIN       | MAX    | MIN         | MAX   |       |
| A        | -         | 0.200  | -           | 5.08  | -     |
| b        | 0.014     | 0.026  | 0.36        | 0.66  | 2     |
| b1       | 0.014     | 0.023  | 0.36        | 0.58  | 3     |
| b2       | 0.045     | 0.065  | 1.14        | 1.65  | -     |
| b3       | 0.023     | 0.045  | 0.58        | 1.14  | 4     |
| c        | 0.008     | 0.018  | 0.20        | 0.46  | 2     |
| c1       | 0.008     | 0.015  | 0.20        | 0.38  | 3     |
| D        | -         | 0.405  | -           | 10.29 | 5     |
| E        | 0.220     | 0.310  | 5.59        | 7.87  | 5     |
| e        | 0.100 BSC |        | 2.54 BSC    |       | -     |
| eA       | 0.300 BSC |        | 7.62 BSC    |       | -     |
| eA/2     | 0.150 BSC |        | 3.81 BSC    |       | -     |
| L        | 0.125     | 0.200  | 3.18        | 5.08  | -     |
| Q        | 0.015     | 0.060  | 0.38        | 1.52  | 6     |
| S1       | 0.005     | -      | 0.13        | -     | 7     |
| $\alpha$ | 90°       | 105°   | 90°         | 105°  | -     |
| aaa      | -         | 0.015  | -           | 0.38  | -     |
| bbb      | -         | 0.030  | -           | 0.76  | -     |
| ccc      | -         | 0.010  | -           | 0.25  | -     |
| M        | -         | 0.0015 | -           | 0.038 | 2, 3  |
| N        | 8         |        | 8           |       | 8     |

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## Dual-In-Line Plastic Packages (PDIP)



### NOTES:

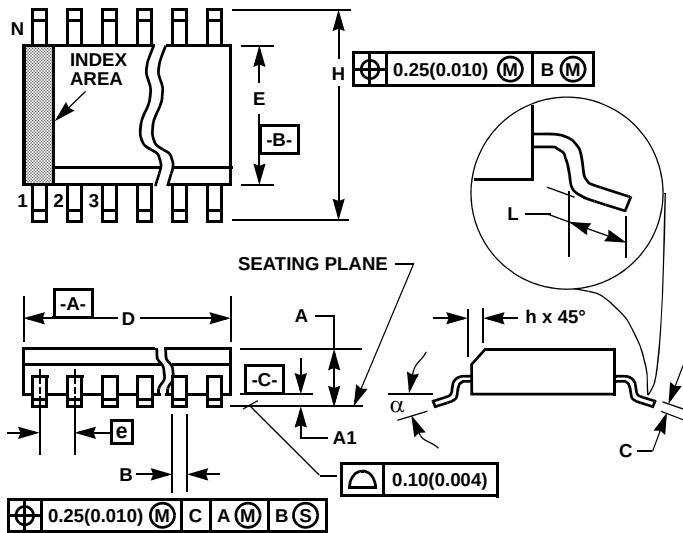
- Controlling Dimensions: INCH. In case of conflict between English and Metric dimensions, the inch dimensions control.
- Dimensioning and tolerancing per ANSI Y14.5M-1982.
- Symbols are defined in the "MO Series Symbol List" in Section 2.2 of Publication No. 95.
- Dimensions A, A1 and L are measured with the package seated in JEDEC seating plane gauge GS-3.
- D, D1, and E1 dimensions do not include mold flash or protrusions. Mold flash or protrusions shall not exceed 0.010 inch (0.25mm).
- E and eA are measured with the leads constrained to be perpendicular to datum -C-.
- eB and eC are measured at the lead tips with the leads unconstrained. eC must be zero or greater.
- B1 maximum dimensions do not include dambar protrusions. Dambar protrusions shall not exceed 0.010 inch (0.25mm).
- N is the maximum number of terminal positions.
- Corner leads (1, N, N/2 and N/2 + 1) for E8.3, E16.3, E18.3, E28.3, E42.6 will have a B1 dimension of 0.030 - 0.045 inch (0.76 - 1.14mm).

### E8.3 (JEDEC MS-001-BA ISSUE D) 8 LEAD DUAL-IN-LINE PLASTIC PACKAGE

| SYMBOL | INCHES    |       | MILLIMETERS |       | NOTES |
|--------|-----------|-------|-------------|-------|-------|
|        | MIN       | MAX   | MIN         | MAX   |       |
| A      | -         | 0.210 | -           | 5.33  | 4     |
| A1     | 0.015     | -     | 0.39        | -     | 4     |
| A2     | 0.115     | 0.195 | 2.93        | 4.95  | -     |
| B      | 0.014     | 0.022 | 0.356       | 0.558 | -     |
| B1     | 0.045     | 0.070 | 1.15        | 1.77  | 8, 10 |
| C      | 0.008     | 0.014 | 0.204       | 0.355 | -     |
| D      | 0.355     | 0.400 | 9.01        | 10.16 | 5     |
| D1     | 0.005     | -     | 0.13        | -     | 5     |
| E      | 0.300     | 0.325 | 7.62        | 8.25  | 6     |
| E1     | 0.240     | 0.280 | 6.10        | 7.11  | 5     |
| e      | 0.100 BSC |       | 2.54 BSC    |       | -     |
| eA     | 0.300 BSC |       | 7.62 BSC    |       | 6     |
| eB     | -         | 0.430 | -           | 10.92 | 7     |
| L      | 0.115     | 0.150 | 2.93        | 3.81  | 4     |
| N      | 8         |       | 8           |       | 9     |

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## Small Outline Plastic Packages (SOIC)



## NOTES:

1. Symbols are defined in the "MO Series Symbol List" in Section 2.2 of Publication Number 95.
2. Dimensioning and tolerancing per ANSI Y14.5M-1982.
3. Dimension "D" does not include mold flash, protrusions or gate burrs. Mold flash, protrusion and gate burrs shall not exceed 0.15mm (0.006 inch) per side.
4. Dimension "E" does not include interlead flash or protrusions. Interlead flash and protrusions shall not exceed 0.25mm (0.010 inch) per side.
5. The chamfer on the body is optional. If it is not present, a visual index feature must be located within the crosshatched area.
6. "L" is the length of terminal for soldering to a substrate.
7. "N" is the number of terminal positions.
8. Terminal numbers are shown for reference only.
9. The lead width "B", as measured 0.36mm (0.014 inch) or greater above the seating plane, shall not exceed a maximum value of 0.61mm (0.024 inch).
10. Controlling dimension: MILLIMETER. Converted inch dimensions are not necessarily exact.

## M8.15 (JEDEC MS-012-AA ISSUE C)

## 8 LEAD NARROW BODY SMALL OUTLINE PLASTIC PACKAGE

| SYMBOL   | INCHES    |        | MILLIMETERS |      | NOTES |
|----------|-----------|--------|-------------|------|-------|
|          | MIN       | MAX    | MIN         | MAX  |       |
| A        | 0.0532    | 0.0688 | 1.35        | 1.75 | -     |
| A1       | 0.0040    | 0.0098 | 0.10        | 0.25 | -     |
| B        | 0.013     | 0.020  | 0.33        | 0.51 | 9     |
| C        | 0.0075    | 0.0098 | 0.19        | 0.25 | -     |
| D        | 0.1890    | 0.1968 | 4.80        | 5.00 | 3     |
| E        | 0.1497    | 0.1574 | 3.80        | 4.00 | 4     |
| e        | 0.050 BSC |        | 1.27 BSC    |      | -     |
| H        | 0.2284    | 0.2440 | 5.80        | 6.20 | -     |
| h        | 0.0099    | 0.0196 | 0.25        | 0.50 | 5     |
| L        | 0.016     | 0.050  | 0.40        | 1.27 | 6     |
| N        | 8         |        | 8           |      | 7     |
| $\alpha$ | 0°        | 8°     | 0°          | 8°   | -     |

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