

FEATURES

- **Ultralow Quiescent Current: 2.1 μ A Typ (LTC1440)**
- **Wide Supply Range:**
Single: 2V to 11V
Dual: ± 1 V to ± 5.5 V
- **Input Voltage Range Includes the Negative Supply**
- **Reference Output Drives 0.01 μ F Capacitor**
- **Adjustable Hysteresis (LTC1440/LTC1442)**
- **TTL/CMOS Compatible Outputs**
- **12 μ s Propagation Delay with 10mV Overdrive**
- **No Crowbar Current**
- **40mA Continuous Source Current**
- **Pin Compatible Upgrades for MAX921/922/923**

APPLICATIONS

- Battery-Powered System Monitoring
- Threshold Detectors
- Window Comparators
- Oscillator Circuits

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DESCRIPTION

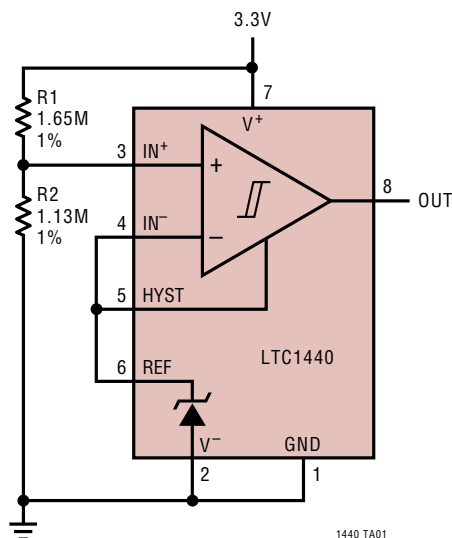
The LTC[®]1440/LTC1441/LTC1442 are ultralow power single and dual comparators with built-in references. The comparators feature less than 3.7 μ A supply current over temperature (LTC1440), a 1.182V $\pm 1\%$ reference, programmable hysteresis (LTC1440/LTC1442) and TTL/CMOS outputs that sink and source current. The reference output can drive a bypass capacitor of up to 0.01 μ F without oscillation.

The comparators operate from a single 2V to 11V supply or a dual ± 1 V to ± 5.5 V supply (LTC1440). Comparator hysteresis is easily programmed by using two resistors and the HYST pin (LTC1440/LTC1442). Each comparator's input operates from the negative supply to within 1.3V of the positive supply. The comparator output stage can continuously source up to 40mA. By eliminating the cross-conducting current that normally happens when the comparator changes logic states, the power supply glitches are eliminated.

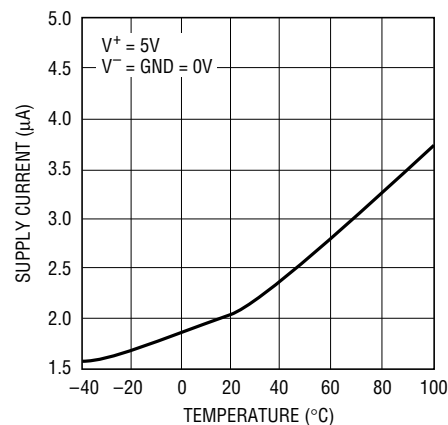
The LTC1440 is available in 8-pin PDIP, SO and MSOP packages. The LTC1441/LTC1442 are available in 8-pin PDIP and SO packages.

TYPICAL APPLICATION

Micropower 2.9V V_{CC} Threshold Detector



LTC1440 Supply Current vs Temperature



1440/1/2 TA02

LTC1440/LTC1441/LTC1442

ABSOLUTE MAXIMUM RATINGS

Voltage

V^+ to V^- , V^+ to GND, GND to V^- 12V to $-0.3V$
 IN^+ , IN^- , HYST ($V^+ + 0.3V$) to ($V^- - 0.3V$)
 REF ($V^+ + 0.3V$) to ($V^- - 0.3V$)
 OUT (LTC1440) ($V^+ + 0.3V$) to (GND $- 0.3V$)
 OUT (LTC1441/LTC1442) ... ($V^+ + 0.3V$) to ($V^- - 0.3V$)

Current

IN^+ , IN^- , HYST 20mA
 REF 20mA
 OUT 50mA

OUT Short-Circuit Duration ($V^+ \leq 5.5V$) Continuous
 Power Dissipation 500mW
 Operating Temperature Range
 LTC144XC $0^\circ C$ to $70^\circ C$
 LTC144XI $-40^\circ C$ to $85^\circ C$
 Storage Temperature Range $-65^\circ C$ to $150^\circ C$
 Lead Temperature (Soldering, 10 sec) $300^\circ C$

PACKAGE/ORDER INFORMATION

TOP VIEW N8 PACKAGE 8-LEAD PDIP S8 PACKAGE 8-LEAD PLASTIC SO $T_{JMAX} = 150^\circ C$, $\theta_{JA} = 130^\circ C/W$ (N8) $T_{JMAX} = 150^\circ C$, $\theta_{JA} = 175^\circ C/W$ (S8)	ORDER PART NUMBER LTC1440CN8 LTC1440CS8 LTC1440IN8 LTC1440IS8 S8 PART MARKING 1440 1440I	TOP VIEW MS8 PACKAGE 8-LEAD PLASTIC MSOP $T_{JMAX} = 150^\circ C$, $\theta_{JA} = 250^\circ C/W$	ORDER PART NUMBER LTC1440CMS8 MS8 PART MARKING LTBX
TOP VIEW N8 PACKAGE 8-LEAD PDIP S8 PACKAGE 8-LEAD PLASTIC SO $T_{JMAX} = 150^\circ C$, $\theta_{JA} = 130^\circ C/W$ (N8) $T_{JMAX} = 150^\circ C$, $\theta_{JA} = 175^\circ C/W$ (S8)	ORDER PART NUMBER LTC1441CN8 LTC1441CS8 LTC1441IN8 LTC1441IS8 S8 PART MARKING 1441 1441I	TOP VIEW N8 PACKAGE 8-LEAD PDIP S8 PACKAGE 8-LEAD PLASTIC SO $T_{JMAX} = 150^\circ C$, $\theta_{JA} = 130^\circ C/W$ (N8) $T_{JMAX} = 150^\circ C$, $\theta_{JA} = 175^\circ C/W$ (S8)	ORDER PART NUMBER LTC1442CN8 LTC1442CS8 LTC1442IN8 LTC1442IS8 S8 PART MARKING 1442 1442I

Consult factory for Military grade parts.

ELECTRICAL CHARACTERISTICS $V^+ = 5V$, $V^- = GND = 0V$, $T_A = 25^\circ C$ unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Power Supply						
V^+	Supply Voltage Range		●	2.0	11.0	V
I_{CC}	Supply Current	$IN^+ = IN^- = 80mV$	●	2.1	3.7	μA
		HYST = REF (LTC1440/LTC1442)	●	3.5	5.7	μA
		LTC1440	●	3.5	5.7	μA
		LTC1441	●			
		LTC1442	●			

ELECTRICAL CHARACTERISTICS $V^+ = 5V$, $V^- = GND = 0V$, $T_A = 25^\circ C$ unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS			MIN	TYP	MAX	UNITS
Comparator								
V _{OS}	Comparator Input Offset Voltage	V _{CM} = 2.5V	●		±3	±10		mV
I _{IN}	Input Leakage Current (I _{IN} ⁺ , I _{IN} ⁻)	V _{IN} ⁺ = V _{IN} ⁻ = 2.5V	●		±0.01	±1.0		nA
	Input Leakage Current (HYST)		●		±0.02	±1.0		nA
V _{CM}	Comparator Input Common Mode Range		●	V ⁻		V ⁺ – 1.3V		V
CMRR	Common Mode Rejection Ratio	V ⁻ to V ⁺ – 1.3V			0.1	1		mV/V
PSRR	Power Supply Rejection Ratio	V ⁺ = 2V to 11V			0.1	1		mV/V
NOISE	Voltage Noise	100Hz to 100kHz			100			μV _{RMS}
V _{HYST}	Hysteresis Input Voltage Range	LTC1440/LTC1442	●	REF – 50mV		REF		V
t _{PD}	Propagation Delay	C _{OUT} = 100pF			15			μs
		Overdrive = 10mV Overdrive = 100mV			8			μs
V _{OH}	Output High Voltage	I _O = –13mA	●	V ⁺ – 0.4V				V
V _{OL}	Output Low Voltage	I _O = 1.8mA	●			GND + 0.4V		V
		LTC1440 LTC1441/LTC1442	●			V ⁻ + 0.4V		V
Reference								
V _{REF}	Reference Voltage	No Load	C Temp Range	●	1.170	1.182	1.194	V
		LTC1440/LTC1442	I Temp Range	●	1.164		1.200	V
		No Load LTC1440 (MSOP)	C Temp Range	●	1.164		1.200	V
I _{SOURCE}	Reference Output Source Current	ΔV _{REF} ≤ 1mV (LTC1442)	●	100	200			μA
I _{SINK}	Reference Output Sink Current	ΔV _{REF} ≤ 2.5mV (LTC1442)		10	20			μA
ΔV _{REF}	Reference Source Current	0 ≤ I _{SOURCE} ≤ 2mA (LTC1440)	●		0.8	5		mV
	Reference Sink Current	0 ≤ I _{SINK} ≤ 10μA (LTC1440)			0.5	1.5		mV
			●				5	
NOISE	Voltage Noise	100Hz to 100kHz			100			μV _{RMS}

 $V^+ = 3V$, $V^- = GND = 0V$, $T_A = 25^\circ C$ unless otherwise noted.**Power Supply**

V^+	Supply Voltage Range		●	2		11	V
I_{CC}	Supply Current	$IN^+ = IN^- = 80mV$	LTC1440	●	2	3.6	μA
		HYST = REF (LTC1440/LTC1442)	LTC1441	●	3.5	5.7	μA
			LTC1442	●	3.5	5.7	μA

Comparator

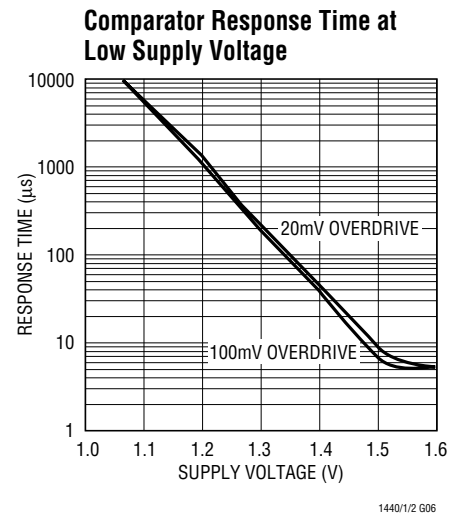
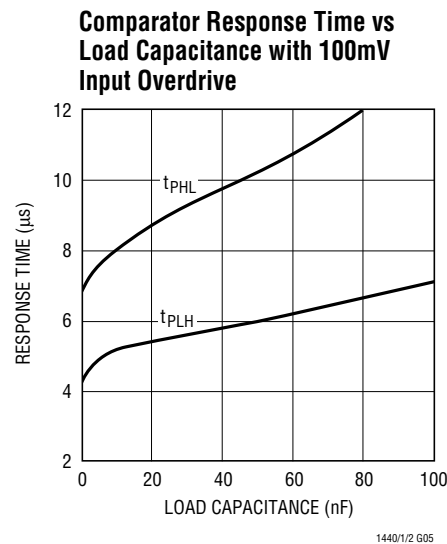
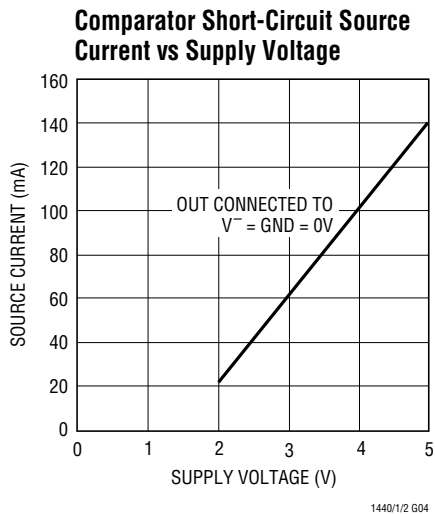
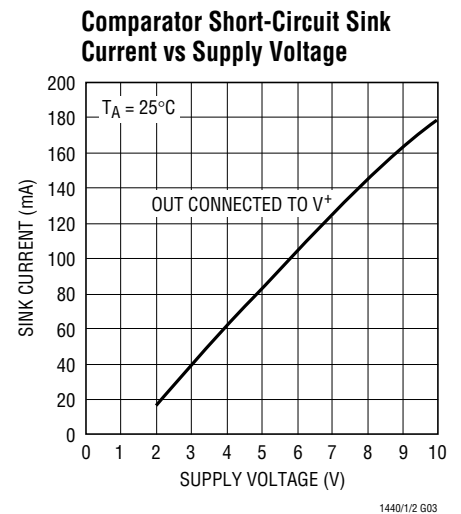
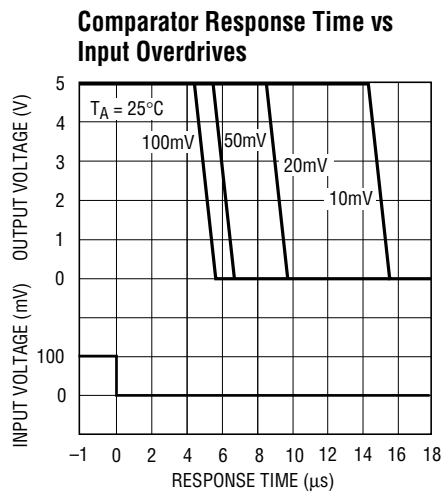
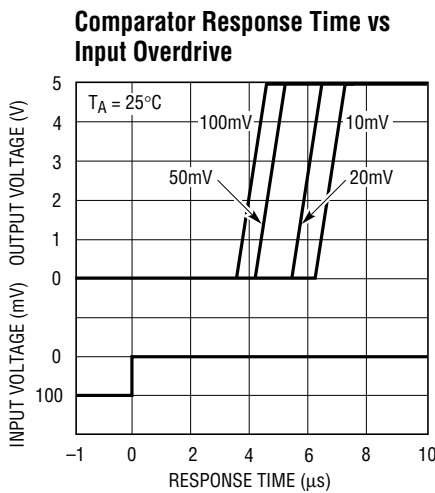
V_{OS}	Comparator Input Offset Voltage	$V_{CM} = 1.5V$	●		± 3	± 10	mV
I_{IN}	Input Leakage Current (IN^+ , IN^-)	$V_{IN^+} = V_{IN^-} = 1.5V$	●		± 0.01	± 1	nA
	Input Leakage Current (HYST)		●		± 0.02	± 1	nA
V_{CM}	Comparator Input Common Mode Range		●	V^-		$V^+ - 1.3V$	V
CMRR	Common Mode Rejection Ratio	V^- to $V^+ - 1.3V$			0.1	1	mV/V
PSRR	Power Supply Rejection Ratio	$V^+ = 2V$ to $11V$			0.1	1	mV/V
NOISE	Voltage Noise	100Hz to 100kHz			100		μV_{RMS}
V_{HYST}	Hysteresis Input Voltage Range	LTC1440/LTC1442	●	REF - 50mV		REF	V
t_{PD}	Propagation Delay	$C_{OUT} = 100pF$			14		μs
		Overdrive = 10mV Overdrive = 100mV			5		μs
V_{OH}	Output High Voltage	$I_O = -8mA$	●	$V^+ - 0.4V$			V
V_{OL}	Output Low Voltage	$I_O = 0.8mA$	●			GND + 0.4V	V
		LTC1440 LTC1441/LTC1442	●			$V^- + 0.4V$	V

ELECTRICAL CHARACTERISTICS $V^+ = 3V$, $V^- = GND = 0V$, $T_A = 25^\circ C$ unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS			MIN	TYP	MAX	UNITS	
Reference									
V _{REF}	Reference Voltage	No Load	C Temp Range	●	1.170	1.182	1.194	V	
		LTC1440/LTC1442	I Temp Range	●	1.164		1.200	V	
		No Load LTC1440 (MSOP)	C Temp Range	●	1.164		1.200	V	
I _{SOURCE}	Reference Output Source Current	ΔV _{REF} ≤ 1mV (LTC1442)			●	60	120	μA	
I _{SINK}	Reference Output Sink Current	ΔV _{REF} ≤ 2.5mV (LTC1442)				10	20	μA	
ΔV _{REF}	Reference Source Current	0 ≤ I _{SOURCE} ≤ 1mA (LTC1440)			●		0.8	5.5	mV
	Reference Sink Current	0 ≤ I _{SINK} ≤ 10μA (LTC1440)			●		0.5	1.5	mV
					●		5	mV	
NOISE	Voltage Noise	100Hz to 100kHz					100		μV _{RMS}

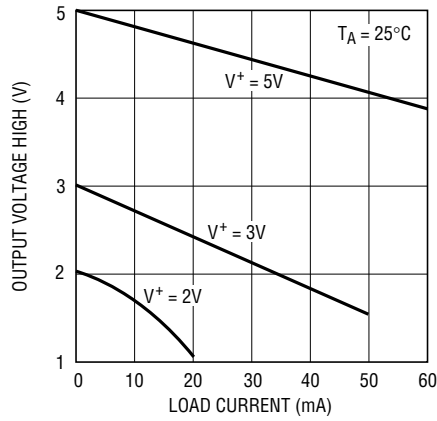
The ● denotes specifications which apply over the full operating temperature range.

TYPICAL PERFORMANCE CHARACTERISTICS



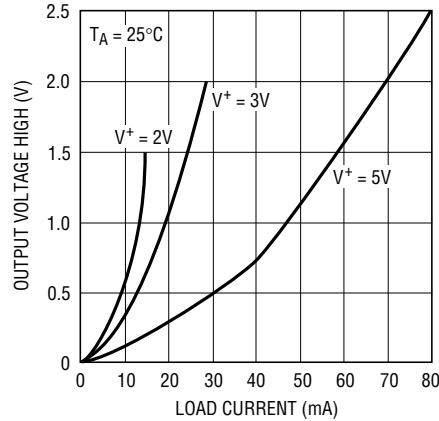
TYPICAL PERFORMANCE CHARACTERISTICS

Comparator Output Voltage High vs Load Current

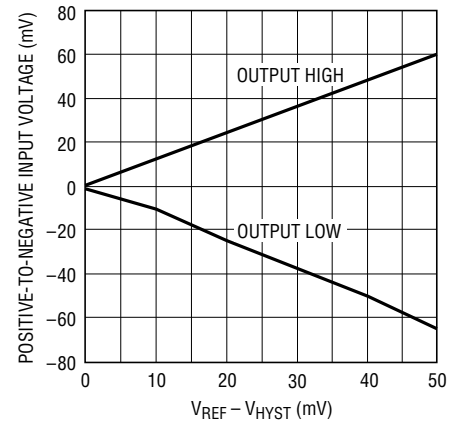


1440/1/2 G07

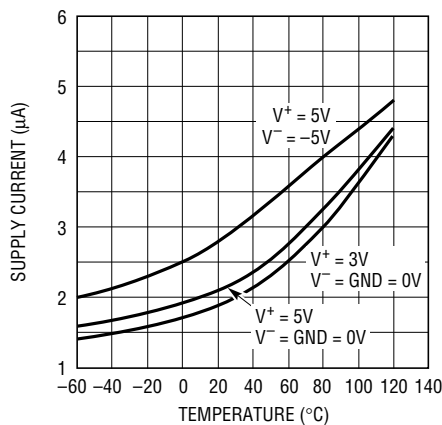
Comparator Output Voltage Low vs Load Current



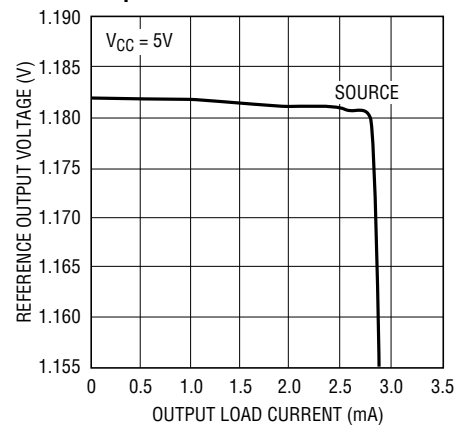
1440/1/2 G08

LTC1440/LTC1442
Hysteresis Control

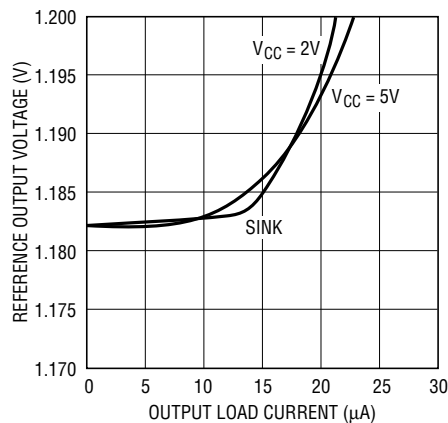
1440/1/2 G09

LTC1440
Supply Current vs Temperature

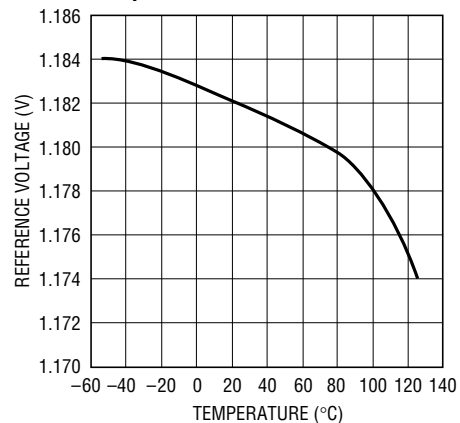
1440/1/2 G10

Reference Output Voltage vs
Output Load Current

1440/1/2 G11

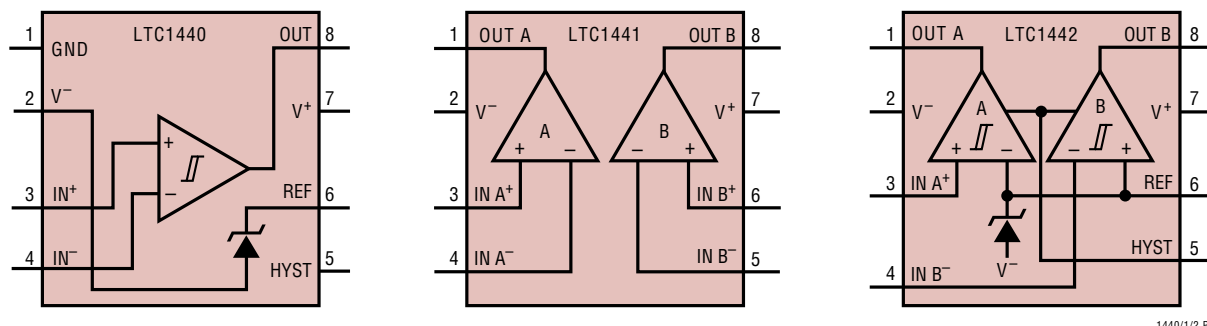
Reference Output Voltage vs
Output Load Current (Sink)

1440/1/2 G12

Reference Voltage vs
Temperature

1440/1/2 G13

PIN FUNCTIONS



1440/1/2 PD

LTC1440

GND (Pin 1): Ground. Connect to V^- for single supply operation.

V^- (Pin 2): Negative Supply. Connect to ground for single supply operation. Potential should be more negative than GND.

IN^+ (Pin 3): Noninverting Comparator Input. Input common mode range from V^- to $V^+ - 1.3V$. Input current typically 10pA at 25°C.

IN^- (Pin 4): Inverting Comparator Input. Input common mode range from V^- to $V^+ - 1.3V$. Input current typically 10pA at 25°C.

HYST (Pin 5): Hysteresis Input. Connect to REF if not used. Input voltage range is from V_{REF} to $V_{REF} - 50mV$.

REF (Pin 6): Reference Output. 1.182V with respect to V^- . Can source up to 200 μA and sink 15 μA at 25°C. Drive 0.01 μF bypass capacitor without oscillation.

V^+ (Pin 7): Positive Supply. 2V to 11V.

OUT (Pin 8): Comparator CMOS Output. Swings from GND to V^+ . Output can source up to 40mA and sink 5mA.

LTC1441

OUT A (Pin 1): Comparator A CMOS Output. Swings from V^- to V^+ . Output can source up to 40mA and sink 5mA.

V^- (Pin 2): Negative Supply.

$IN A^+$ (Pin 3): Noninverting Input of Comparator A. Input common mode range from V^- to $V^+ - 1.3V$. Input current typically 10pA at 25°C.

$IN A^-$ (Pin 4): Inverting Input of Comparator A. Input common mode range from V^- to $V^+ - 1.3V$. Input current typically 10pA at 25°C.

$IN B^-$ (Pin 5): Inverting Input of Comparator B. Input common mode range from V^- to $V^+ - 1.3V$. Input current typically 10pA at 25°C.

$IN B^+$ (Pin 6): Noninverting Input of Comparator B. Input common mode range from V^- to $V^+ - 1.3V$. Input current typically 10pA at 25°C.

V^+ (Pin 7): Positive Supply. 2V to 11V.

OUT B (Pin 8): Comparator B CMOS Output. Swings from V^- to V^+ . Output can source up to 40mA and sink 5mA.

LTC1442

OUT A (Pin 1): Comparator A CMOS Output. Swings from V^- to V^+ . Output can source up to 40mA and sink 5mA.

V^- (Pin 2): Negative Supply.

$IN A^+$ (Pin 3): Noninverting Input of Comparator A. Input common mode range from V^- to $V^+ - 1.3V$. Input current typically 10pA at 25°C.

$IN B^-$ (Pin 4): Inverting Input of Comparator B. Input common mode range from V^- to $V^+ - 1.3V$. Input current typically 10pA at 25°C.

HYST (Pin 5): Hysteresis Input. Connect to REF if not used. Input voltage range is from V_{REF} to $V_{REF} - 50mV$.

REF (Pin 6): Reference Output. 1.182V with respect to V^- . Can source up to 200 μA and sink 15 μA at 25°C. Drive 0.01 μF bypass capacitor without oscillation.

V^+ (Pin 7): Positive Supply. 2V to 11V.

OUT B (Pin 8): Comparator B CMOS Output. Swings from V^- to V^+ . Output can source up to 40mA and sink 5mA.

APPLICATIONS INFORMATION

LTC1440/LTC1441/LTC1442 are a family of micropower comparators with built-in 1.182V reference. Features include programmable hysteresis (LTC1440/LTC1442), wide supply voltage range (2V to 11V) and the ability of the reference to drive up to a $0.01\mu\text{F}$ capacitor without oscillation. The comparators' CMOS outputs can source up to 40mA and the supply current glitches, that normally occur when switching logic states, have been eliminated.

Power Supplies

The comparator family operates from a single 2V to 11V supply. The LTC1440 includes a separate ground for the comparator output stage, allowing a split supply ranging from $\pm 1\text{V}$ to $\pm 5.5\text{V}$. Connecting V^- to GND on the LTC1440 will allow single supply operation. If the comparator output is required to source more than 1mA, or the supply source impedance is high, V^+ should be bypassed with a $0.1\mu\text{F}$ capacitor.

Comparator Inputs

The comparator inputs can swing from the negative supply V^- to within 1.3V max of the positive supply V^+ . The inputs can be forced 300mV below V^- or above V^+ without damage and the typical input leakage current is only $\pm 10\text{pA}$.

Comparator Outputs

The LTC1440 comparator output swings between GND and V^+ to assure TTL compatibility with a split supply. The LTC1441 and LTC1442 outputs swing between V^- and V^+ . The outputs are capable of sourcing up to 40mA and sinking up to 5mA while still maintaining microampere quiescent currents. The output stage does not generate crowbar switching currents during transitions which helps minimize parasitic feedback through the supply pins.

Voltage Reference

The internal bandgap reference has a voltage of 1.182V referenced to V^- . The reference accuracy is 1.5% from -40°C to 85°C . It can source up to $200\mu\text{A}$ and sink up to $20\mu\text{A}$ with a 5V supply. The reference can drive a bypass

capacitor of up to $0.01\mu\text{F}$ without oscillation and by inserting a series resistor, capacitance values up to $100\mu\text{F}$ can be used (Figure 1).

Figure 2 shows the resistor value required for different capacitor values to achieve critical damping. Bypassing the reference can help prevent false tripping of the comparators by preventing glitches on V^+ or reference load transients from disturbing the reference output voltage.

Figure 3 shows the bypassed reference output with a square wave applied to the V^+ pin. Resistors R2 and R3 set 10mV of hysteresis voltage band while R1 damps the reference response. Note that the comparator output doesn't trip.

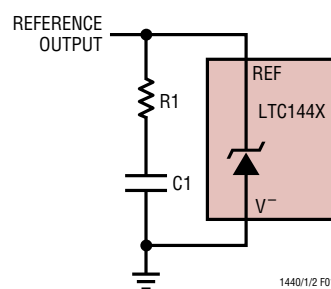


Figure 1. Damping the Reference Output

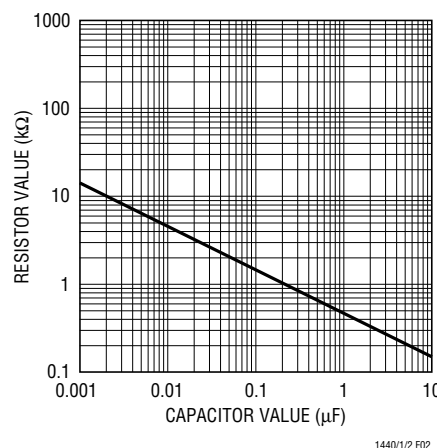


Figure 2. Damping Resistance vs Bypass Capacitor Value

APPLICATIONS INFORMATION

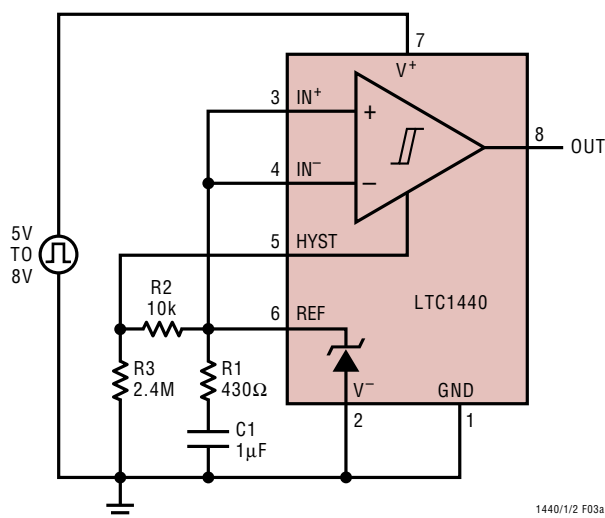


Figure 3a. Reference Transient Response Test Circuit

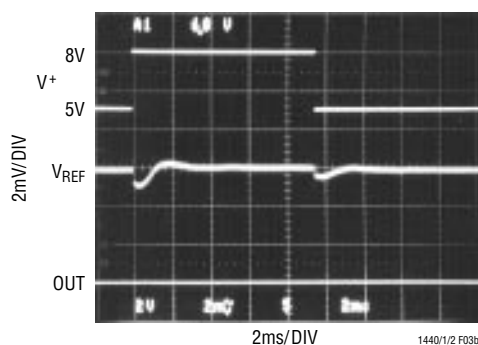


Figure 3b. Reference and Comparator Output Transient Response

Hysteresis

Hysteresis can be added to the LTC1440 by connecting a resistor (R1) between the REF and HYST pins and a second resistor (R2) from HYST to V⁻ (Figure 4).

The difference between the upper and lower threshold voltages, or hysteresis voltage band (V_{HB}), is equal to twice the voltage difference between the REF and HYST pins.

When more hysteresis is added, the upper threshold increases the same amount as the low threshold decreases. The maximum voltage allowed between REF and HYST pins is 50mV, producing a maximum hysteresis voltage band of 100mV. The hysteresis band could vary by

up to 15%. If hysteresis is not wanted, the HYST pin should be shorted to REF. Acceptable values for I_{REF} range from 0.1μA to 5μA. If 2.4M is chosen for R2, then the value of R1 is equal to the value of V_{HB}.

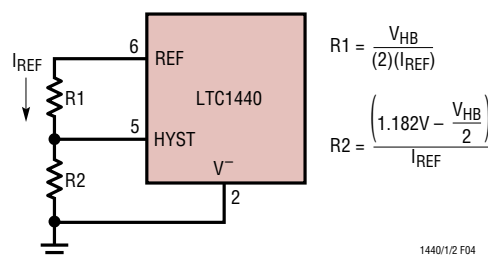


Figure 4. Programmable Hysteresis

Level Detector

The LTC1440 is ideal for use as a micropower level detector as shown in Figure 5. R1 and R2 form a voltage divider from V_{IN} to the noninverting comparator input. R3 and R4 set the hysteresis voltage, and R5 and C1 bypass the reference output. The following design procedure can be used to select the component values:

1. Choose the V_{IN} voltage trip level, in this example 4.65V.

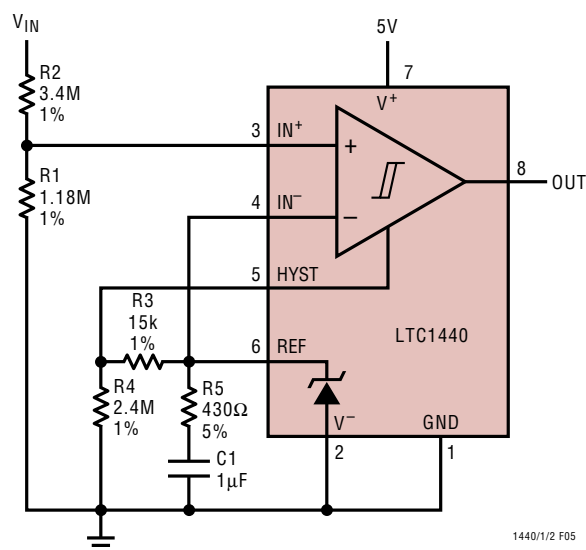


Figure 5. Glitch-Free Level Detector with Hysteresis

APPLICATIONS INFORMATION

2. Calculate the required resistive divider ratio.

$$\text{Ratio} = V_{\text{REF}}/V_{\text{IN}}$$

$$\text{Ratio} = 1.182\text{V}/4.65\text{V} = 0.254$$

3. Choose the required hysteresis voltage band at the input V_{HBIN} , in this example 60mV. Calculate the hysteresis voltage band referred to the comparator input V_{HB} .

$$V_{\text{HB}} = (V_{\text{HBIN}})(\text{Ratio})$$

$$V_{\text{HB}} = (60\text{mV})(0.254)$$

$$V_{\text{HB}} = 15.24\text{mV}$$

4. Choose the values for R3 and R4 to set the hysteresis.

$$R4 = 2.4\text{M}$$

$$R3(\text{k}\Omega) = V_{\text{HB}} = 15\text{k}$$

5. Choose the values for R1 and R2 to set the trip point.

$$R1 = \frac{V_{\text{REF}}}{I_{\text{BIAS}}} = \frac{1.182\text{V}}{1\mu\text{A}} = 1.18\text{M}$$

$$R2 = R1 \left[\frac{V_{\text{IN}}}{V_{\text{REF}} + \frac{V_{\text{HB}}}{2}} - 1 \right]$$

$$R2 = 1.18\text{M} \left[\frac{4.65\text{V}}{1.182\text{V} + \frac{15\text{mV}}{2}} - 1 \right]$$

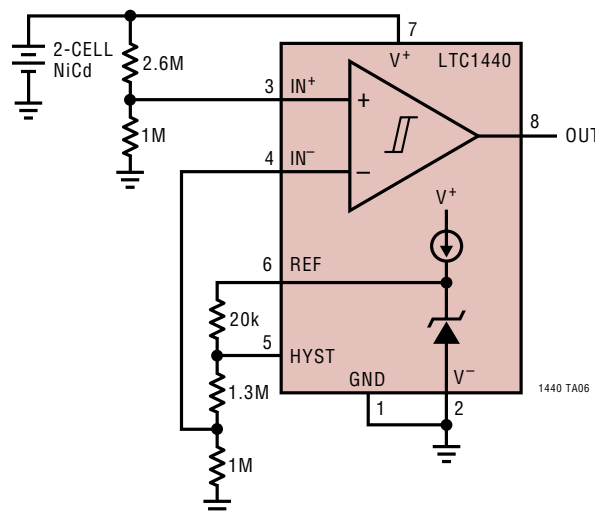
$$R2 = 3.40\text{M}$$

Low Voltage Operation

The guaranteed minimum operating voltage is 2V or ($\pm 1\text{V}$). As the total supply voltage is reduced below 2V, the performance degrades and the supply current falls. Both the reference and the comparator are still functioning with supply voltage down to 1.5V. The reference will have slightly worse temperature coefficient. While the comparator would have less output drive and the propagation delay increases. The useful input voltage range extends from the negative supply to a little under 1V below the positive supply, which is slightly closer to the positive rail than the device operating from higher supply voltages.

TYPICAL APPLICATIONS

Low Battery Detect Operates Down to 1.5V



The schematic diagram illustrates the TRW-IRC MRT-5 temperature sensor interface circuit. Key components and their connections include:

- Power Supply:** A 5V supply is connected to the circuit.
- LT1034 1.2V Diode:** Connected in series with the 5V supply.
- 2N3809 JFET:** Its gate is connected to the 5V supply through a 150k resistor. The source is connected to ground, and the drain is connected to the 1.2V diode.
- 74C00 NAND Gate:** One input is connected to the drain of the 2N3809 JFET. The other input is connected to ground. The output is connected to the STATUS signal line.
- 74C74 Flip-Flop:** The D input is connected to the STATUS signal line. The Q output is connected to the DATA OUT signal line. The CLK input is connected to the 5V supply.
- LTC1441 Comparators:** Two comparators, C1 and C2, are used. C1 has its non-inverting input (+) connected to the 5V supply and its inverting input (-) connected to the drain of the 2N3809 JFET. C2 has its non-inverting input (+) connected to the 5V supply and its inverting input (-) connected to the drain of the 2N3809 JFET. The outputs of both comparators are connected to the STATUS signal line.
- Resistors:** A 100k resistor is connected between the 5V supply and the drain of the 2N3809 JFET. A 365k resistor is connected between the 5V supply and the drain of the 2N3809 JFET. A 10M resistor is connected between the 5V supply and the drain of the 2N3809 JFET.
- Capacitors:** A 0.033μF POLYSTYRENE capacitor is connected between the 5V supply and the drain of the 2N3809 JFET. A 10pF capacitor is connected between the 5V supply and the drain of the 2N3809 JFET.
- Other Components:** A 1N914 diode is connected between the 5V supply and the drain of the 2N3809 JFET. A 10M resistor is connected between the 5V supply and the drain of the 2N3809 JFET.

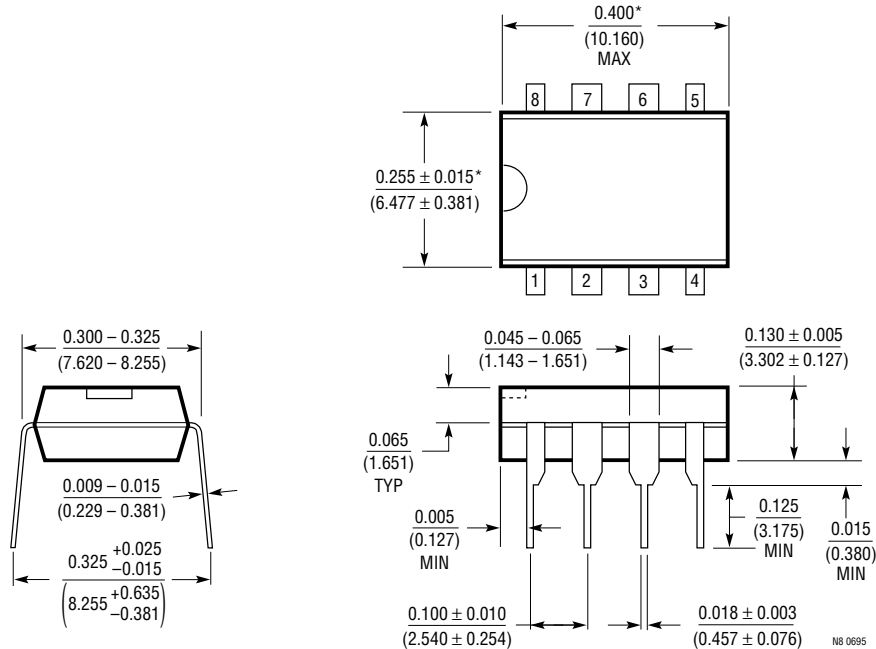
*TRW-IRC MRT-5/+120ppm/°C

1440/1/2 TA03

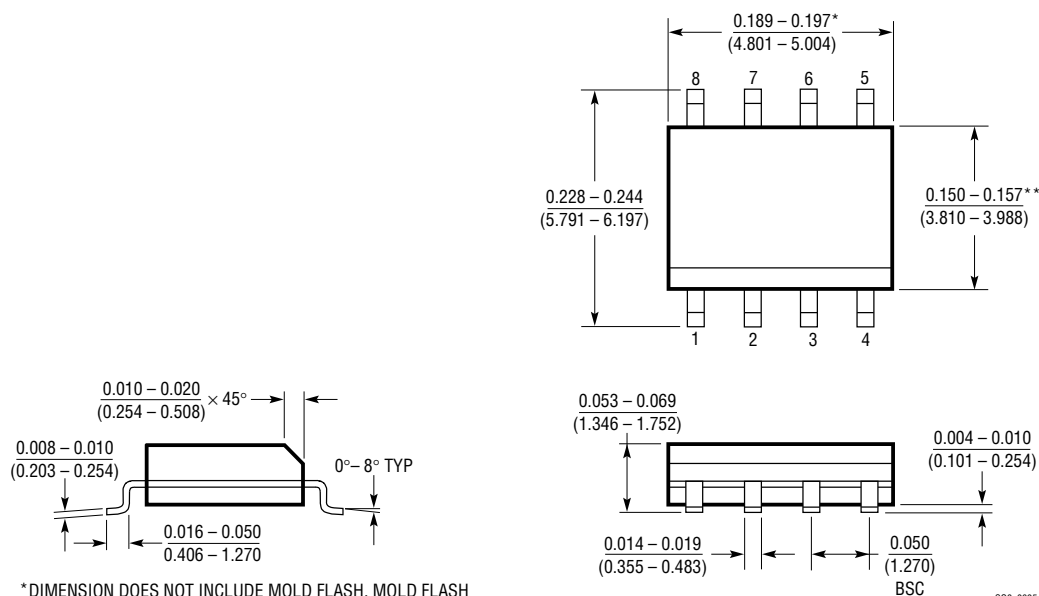
PACKAGE DESCRIPTION

Dimensions in inches (millimeters) unless otherwise noted.

N8 Package
8-Lead PDIP (Narrow 0.300)
 (LTC DWG # 05-08-1510)

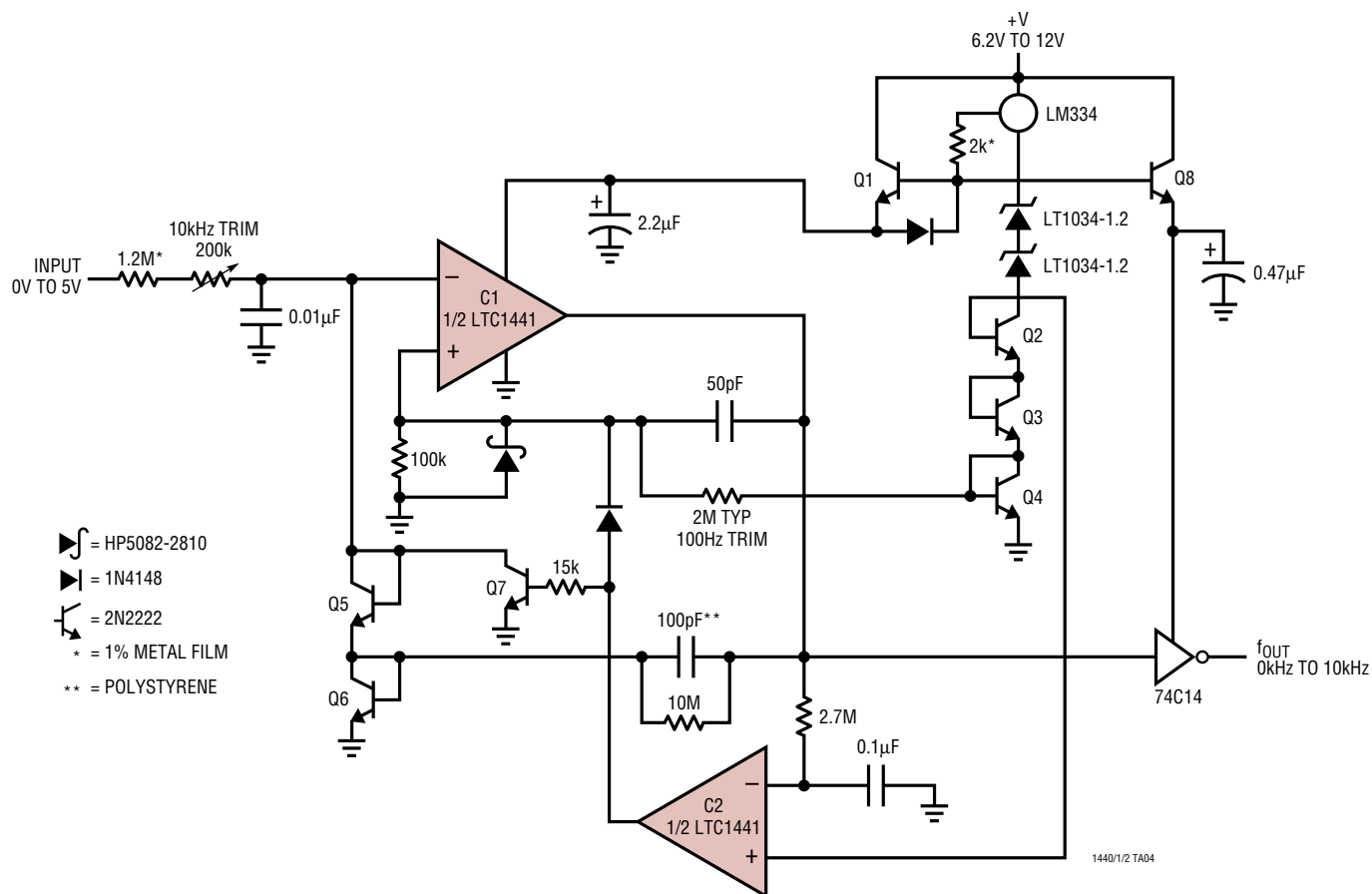


S8 Package
8-Lead Plastic Small Outline (Narrow 0.150)
 (LTC DWG # 05-08-1610)



TYPICAL APPLICATION

10kHz V/F Converter



RELATED PARTS

PART NUMBER	DESCRIPTION	COMMENTS
LT1178/LT1179	Dual/Quad 17μA Precision Single Supply Op Amps	70μV Max V _{OS} , 5nA Max I _{BIAS}
LT1351	Single 250μA, 3MHz, 200V/μs Op Amp with Shutdown	C-Load™ Op Amp Stable Driving Any Capacitive Load
LT1352/LT1353	Dual/Quad 250μA, 3MHz, 200V/μs Op Amps	C-Load Op Amps Stable Driving Any Capacitive Load
LTC1443/LTC1444/LTC1445	Micropower Quad Comparator with 1% Reference	LTC1443 Has 1.182V Reference, LTC1444/LTC1445 Have 1.221V Reference and Adjustable Hysteresis

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