

NCE P-Channel Enhancement Mode Power MOSFET

Description

The NCE30P30L uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. This device is well suited for high current load applications.

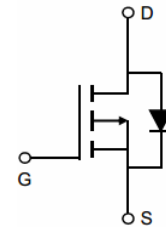
General Features

- $V_{DS} = -30V, I_D = -30A$
 $R_{DS(ON)} < 19m\Omega @ V_{GS} = -10V$
 $R_{DS(ON)} < 30m\Omega @ V_{GS} = -4.5V$
- High density cell design for ultra low $R_{DS(on)}$
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation
- **Pb free terminal plating**
- **RoHS compliant**
- **Halogen free**

Application

- High side switch for full bridge converter
- DC/DC converter for LCD display

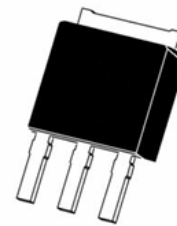
100% UIS TESTED!
100% ΔV_{ds} TESTED!



Schematic diagram



Marking and pin assignment



TO-251S-3L top view

Package Marking and Ordering Information

| Device Marking | Device | Device Package | Reel Size | Tape width | Quantity |
|----------------|-----------|----------------|-----------|------------|----------|
| NCE30P30L | NCE30P30L | TO-25IS | - | - | - |

Absolute Maximum Ratings ($T_C = 25^\circ C$ unless otherwise noted)

| Parameter | Symbol | Limit | Unit |
|---|--------------------|------------|---------------|
| Drain-Source Voltage | V_{DS} | -30 | V |
| Gate-Source Voltage | V_{GS} | ± 20 | V |
| Drain Current-Continuous | I_D | -30 | A |
| Drain Current-Continuous($T_C = 100^\circ C$) | $I_D(100^\circ C)$ | -21.2 | A |
| Pulsed Drain Current | I_{DM} | -70 | A |
| Maximum Power Dissipation | P_D | 60 | W |
| Derating factor | | 0.4 | W/ $^\circ C$ |
| Single pulse avalanche energy ^(Note 5) | E_{AS} | 169 | mJ |
| Operating Junction and Storage Temperature Range | T_J, T_{STG} | -55 To 175 | $^\circ C$ |

Thermal Characteristic

| | | | |
|--|-----------------|-----|--------------|
| Thermal Resistance, Junction-to-Case ^(Note 2) | $R_{\theta JC}$ | 2.5 | $^\circ C/W$ |
|--|-----------------|-----|--------------|

Electrical Characteristics (T_C=25°C unless otherwise noted)

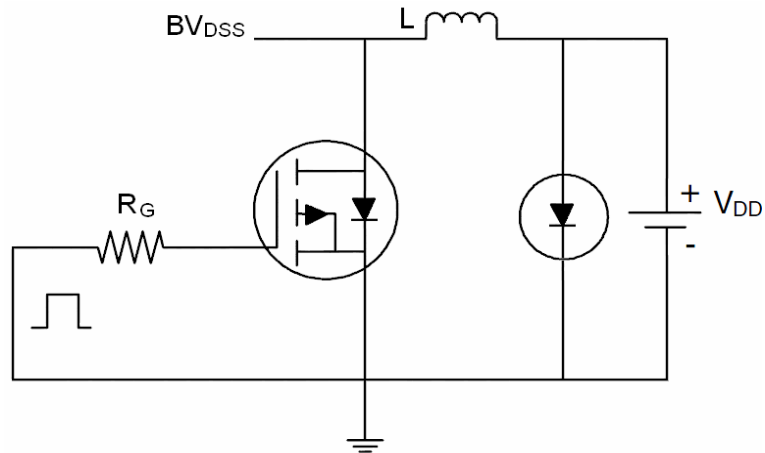
| Parameter | Symbol | Condition | Min | Typ | Max | Unit |
|---|---------------------|---|------|------|------|------|
| Off Characteristics | | | | | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | V _{GS} =0V, I _D =-250μA | -30 | - | - | V |
| Zero Gate Voltage Drain Current | I _{DSS} | V _{DS} =-30V, V _{GS} =0V | - | - | -1 | μA |
| Gate-Body Leakage Current | I _{GSS} | V _{GS} =±20V, V _{DS} =0V | - | - | ±100 | nA |
| On Characteristics (Note 3) | | | | | | |
| Gate Threshold Voltage | V _{GS(th)} | V _{DS} =V _{GS} , I _D =-250μA | -1.2 | -1.6 | -2.5 | V |
| Drain-Source On-State Resistance | R _{DS(ON)} | V _{GS} =-10V, I _D =-15A | - | 16 | 19 | mΩ |
| | | V _{GS} =-4.5V, I _D =-15A | - | 22 | 30 | mΩ |
| Forward Transconductance | g _{FS} | V _{DS} =-5V, I _D =-15A | - | 25 | - | S |
| Dynamic Characteristics (Note 4) | | | | | | |
| Input Capacitance | C _{iss} | V _{DS} =-15V, V _{GS} =0V, F=1.0MHz | - | 1363 | - | PF |
| Output Capacitance | C _{oss} | | - | 250 | - | PF |
| Reverse Transfer Capacitance | C _{rss} | | - | 210 | - | PF |
| Switching Characteristics (Note 4) | | | | | | |
| Turn-on Delay Time | t _{d(on)} | V _{DD} =-30V, R _L =3Ω, V _{GS} =-10V, R _G =2.5Ω | - | 9 | - | nS |
| Turn-on Rise Time | t _r | | - | 10 | - | nS |
| Turn-Off Delay Time | t _{d(off)} | | - | 50 | - | nS |
| Turn-Off Fall Time | t _f | | - | 20 | - | nS |
| Total Gate Charge | Q _g | V _{DS} =-15, I _D =-15A, V _{GS} =-10V | - | 31.2 | - | nC |
| Gate-Source Charge | Q _{gs} | | - | 3.2 | - | nC |
| Gate-Drain Charge | Q _{gd} | | - | 9.2 | - | nC |
| Drain-Source Diode Characteristics | | | | | | |
| Diode Forward Voltage (Note 3) | V _{SD} | V _{GS} =0V, I _S =-15A | - | - | -1.2 | V |
| Diode Forward Current (Note 2) | I _S | | - | - | -30 | A |
| Reverse Recovery Time | t _{rr} | T _J = 25°C, I _F = -15A | - | 24 | - | nS |
| Reverse Recovery Charge | Q _{rr} | di/dt = -100A/μs (Note 3) | - | 16 | - | nC |

Notes:

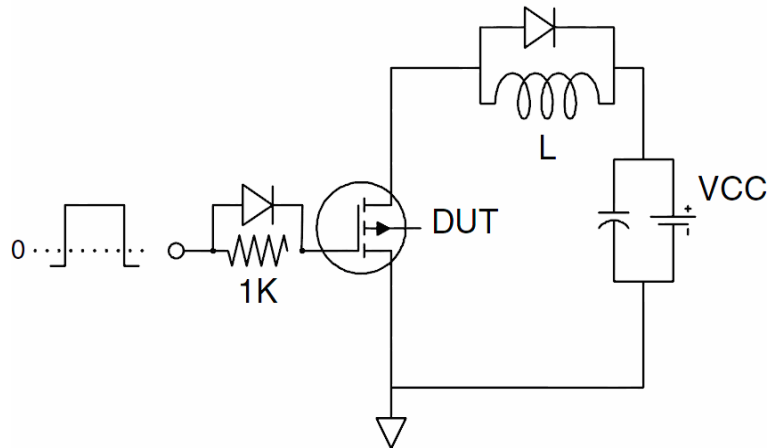
1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production
5. E_{AS} condition: T_J=25°C, V_{DD}=-15V, V_G=-10V, L=0.5mH, R_G=25Ω

Test Circuit

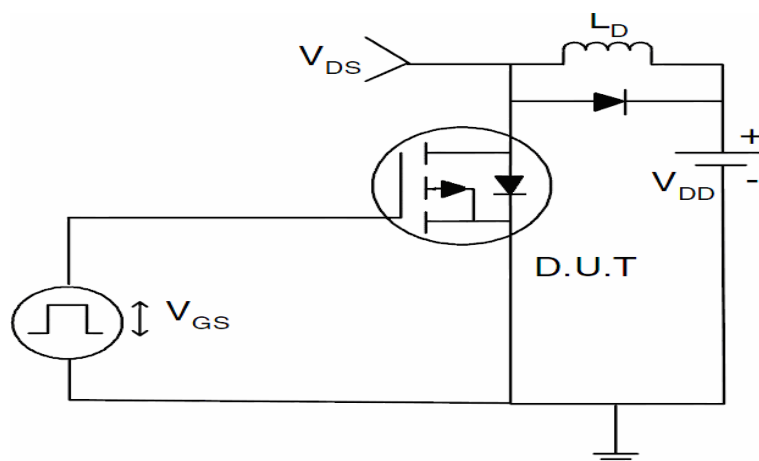
1) E_{AS} Test Circuit



2) Gate Charge Test Circuit



3) Switch Time Test Circuit



Typical Electrical and Thermal Characteristics (Curves)

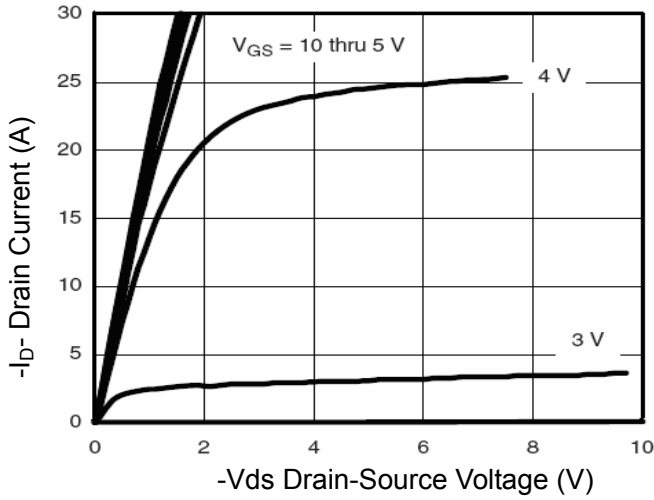


Figure 1 Output Characteristics

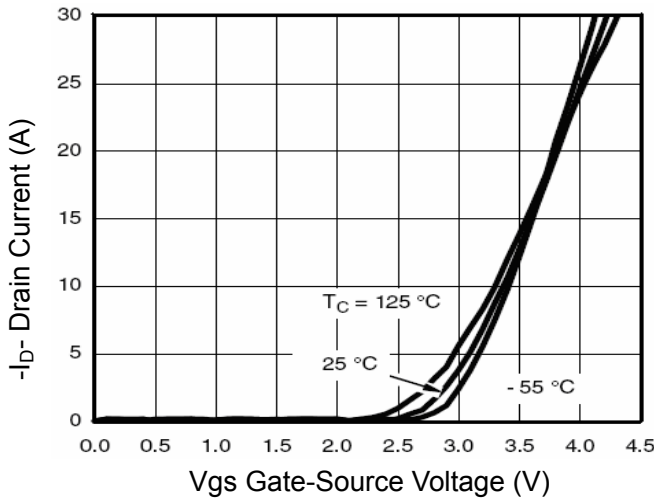


Figure 2 Transfer Characteristics

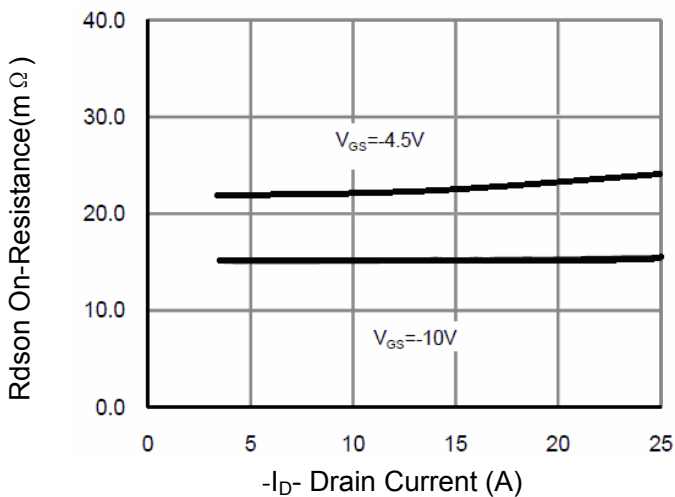


Figure 3 Rdson- Drain Current

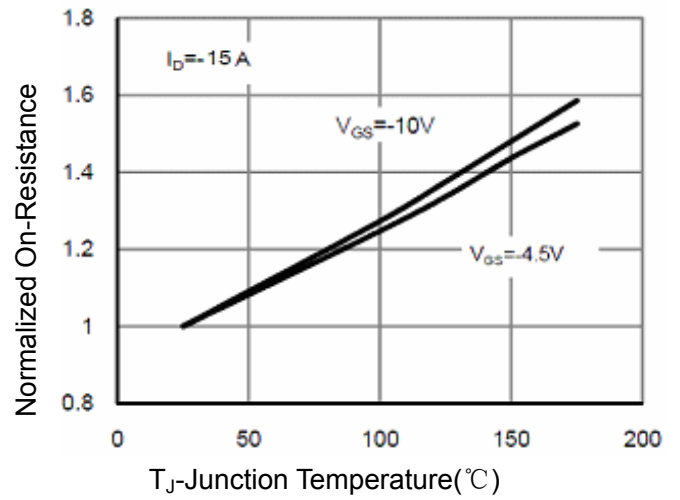


Figure 4 Rdson-Junction Temperature

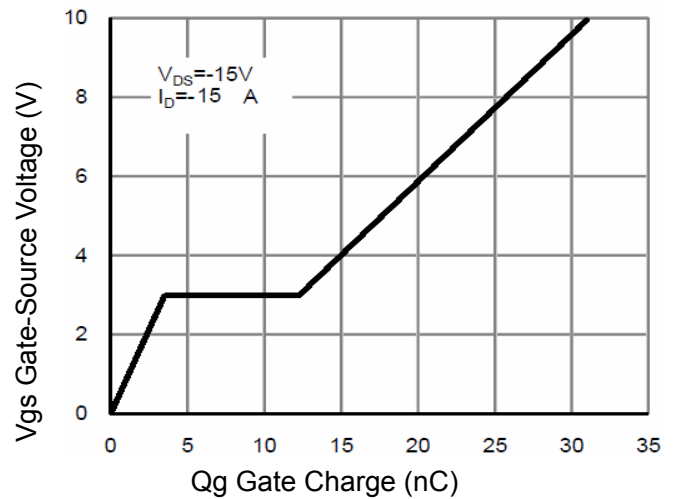


Figure 5 Gate Charge

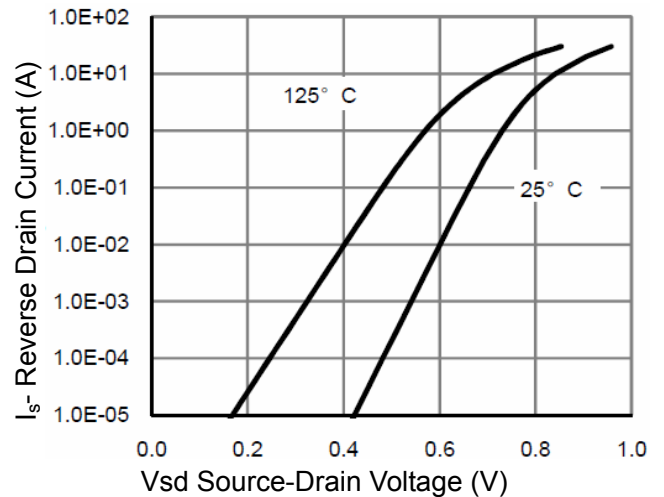


Figure 6 Source- Drain Diode Forward

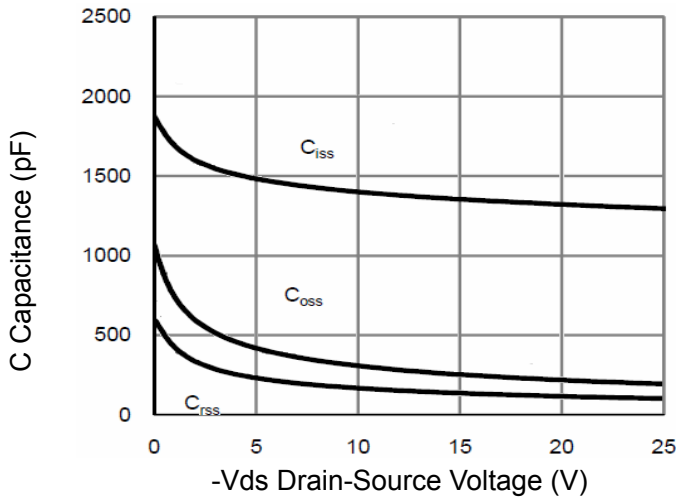


Figure 7 Capacitance vs Vds

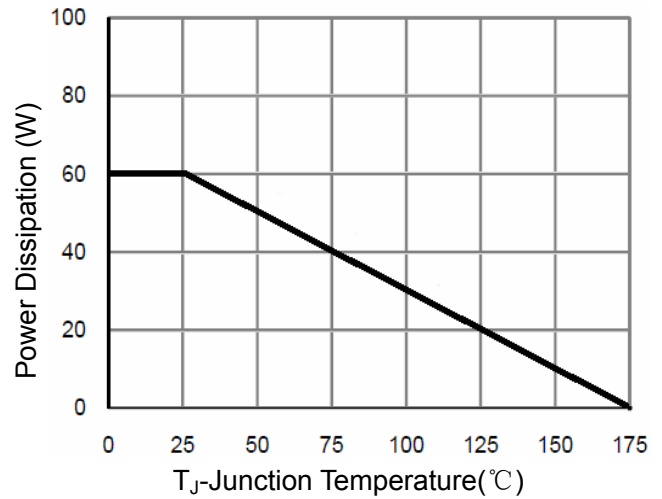


Figure 9 Power De-rating

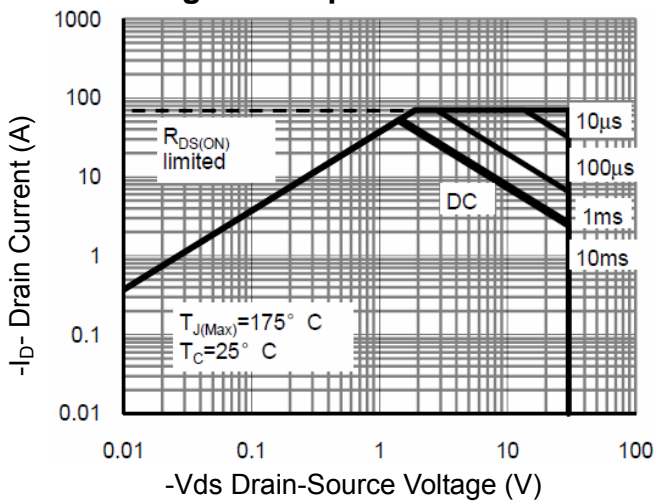


Figure 8 Safe Operation Area

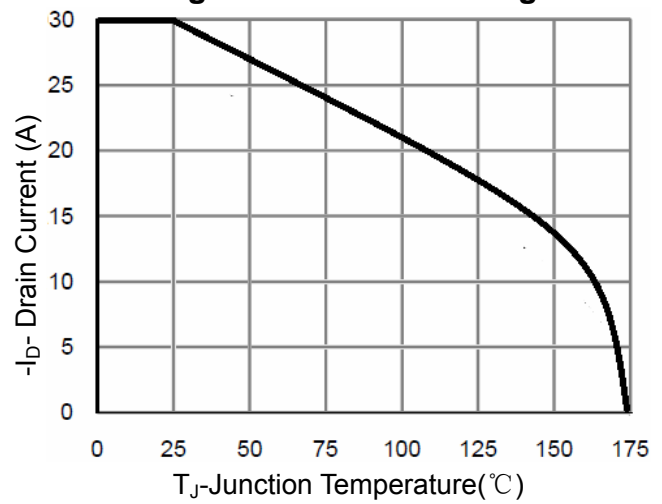


Figure 10 ID Current Derating

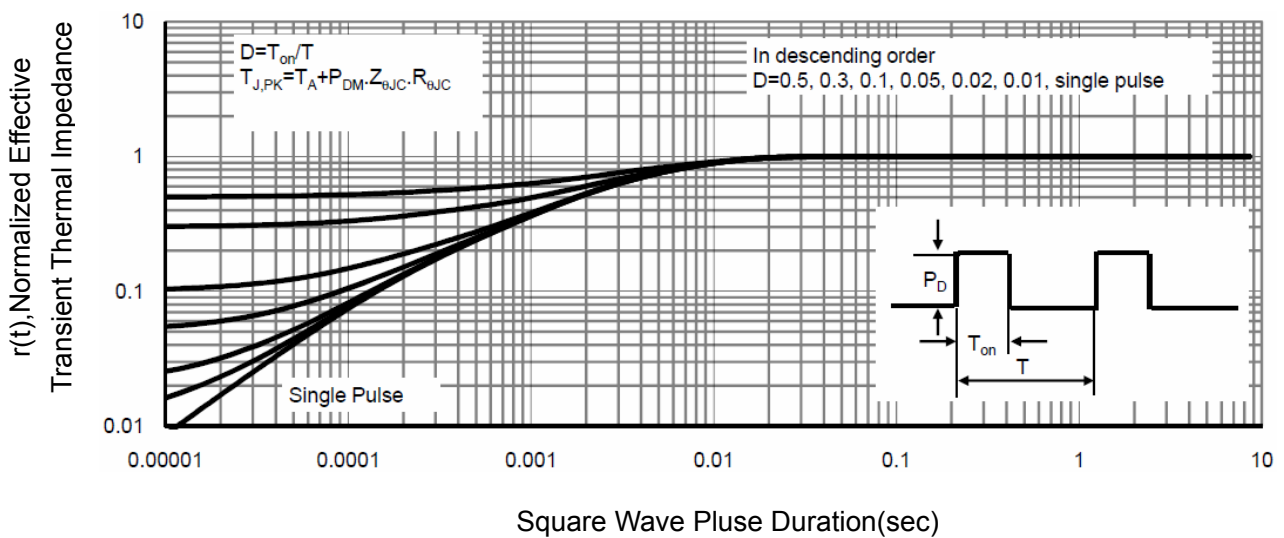
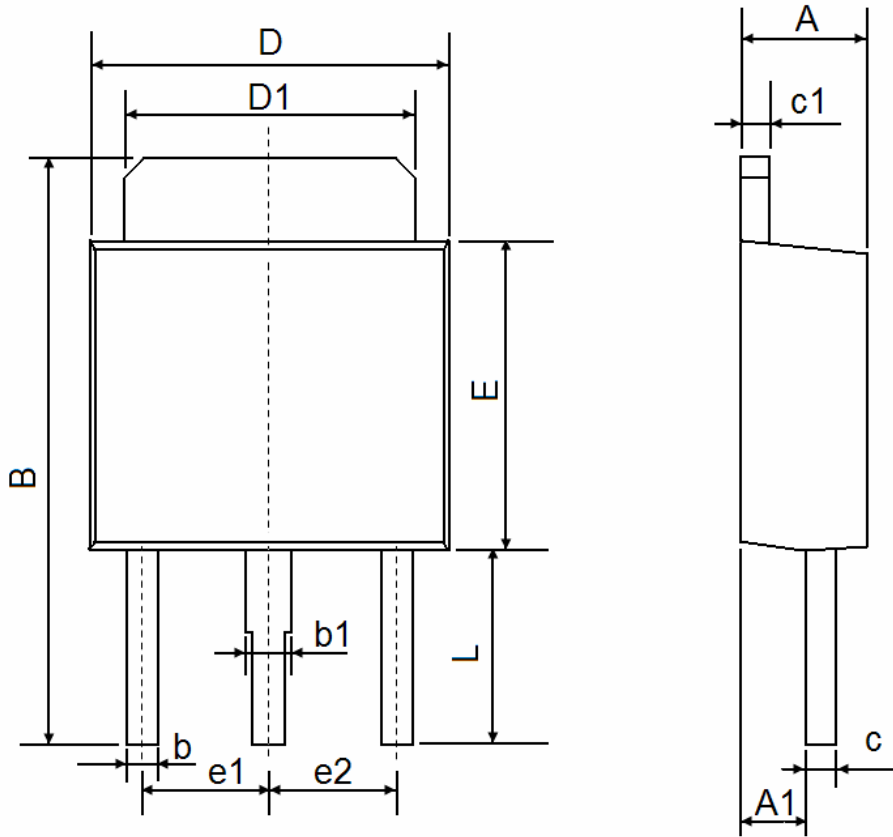


Figure 11 Normalized Maximum Transient Thermal Impedance

TO-251S Package Information



| Symbol | Dimensions In Millimeters | | Dimensions In Inches | |
|--------|---------------------------|--------|----------------------|-------|
| | Min. | Max. | Min. | Max. |
| A | 2.250 | 2.350 | 0.089 | 0.093 |
| A1 | 1.150 | 1.250 | 0.045 | 0.049 |
| B | 10.200 | 10.800 | 0.402 | 0.425 |
| b | 0.550 | 0.650 | 0.022 | 0.026 |
| b1 | 0.750 | 0.850 | 0.030 | 0.033 |
| c | 0.480 | 0.540 | 0.019 | 0.021 |
| c1 | 0.480 | 0.540 | 0.019 | 0.021 |
| D | 6.400 | 6.600 | 0.252 | 0.260 |
| D1 | 5.250 | 5.350 | 0.207 | 0.211 |
| E | 5.400 | 5.600 | 0.213 | 0.220 |
| e1 | 2.300 TYP | | 0.091 TYP | |
| e2 | 2.300 TYP | | 0.091 TYP | |
| L | 3.300 | 3.700 | 0.130 | 0.146 |

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